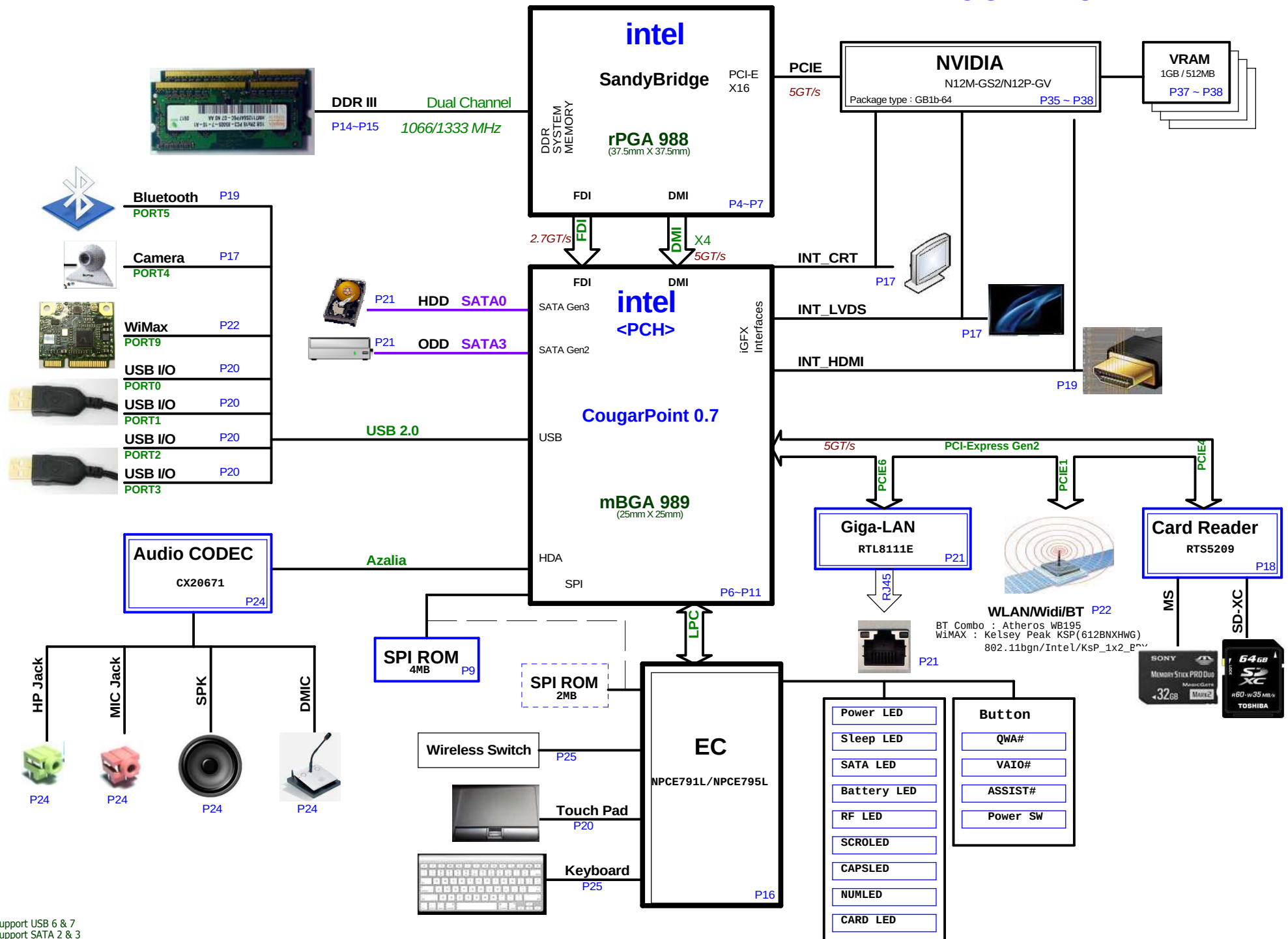


EO : For DIS GFX only
IO : For INT GFX only



Change List from PVT to MP

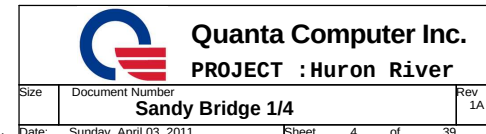
HK1_MB_SCH_PVT_001

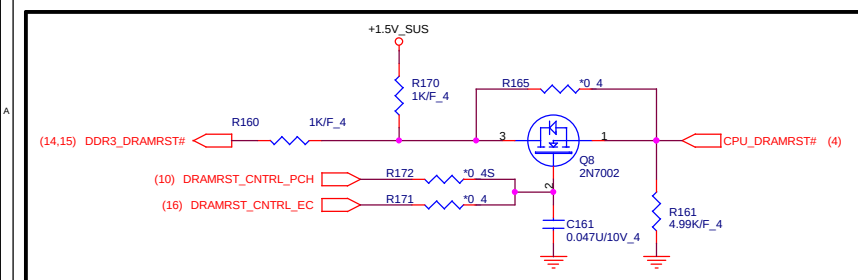
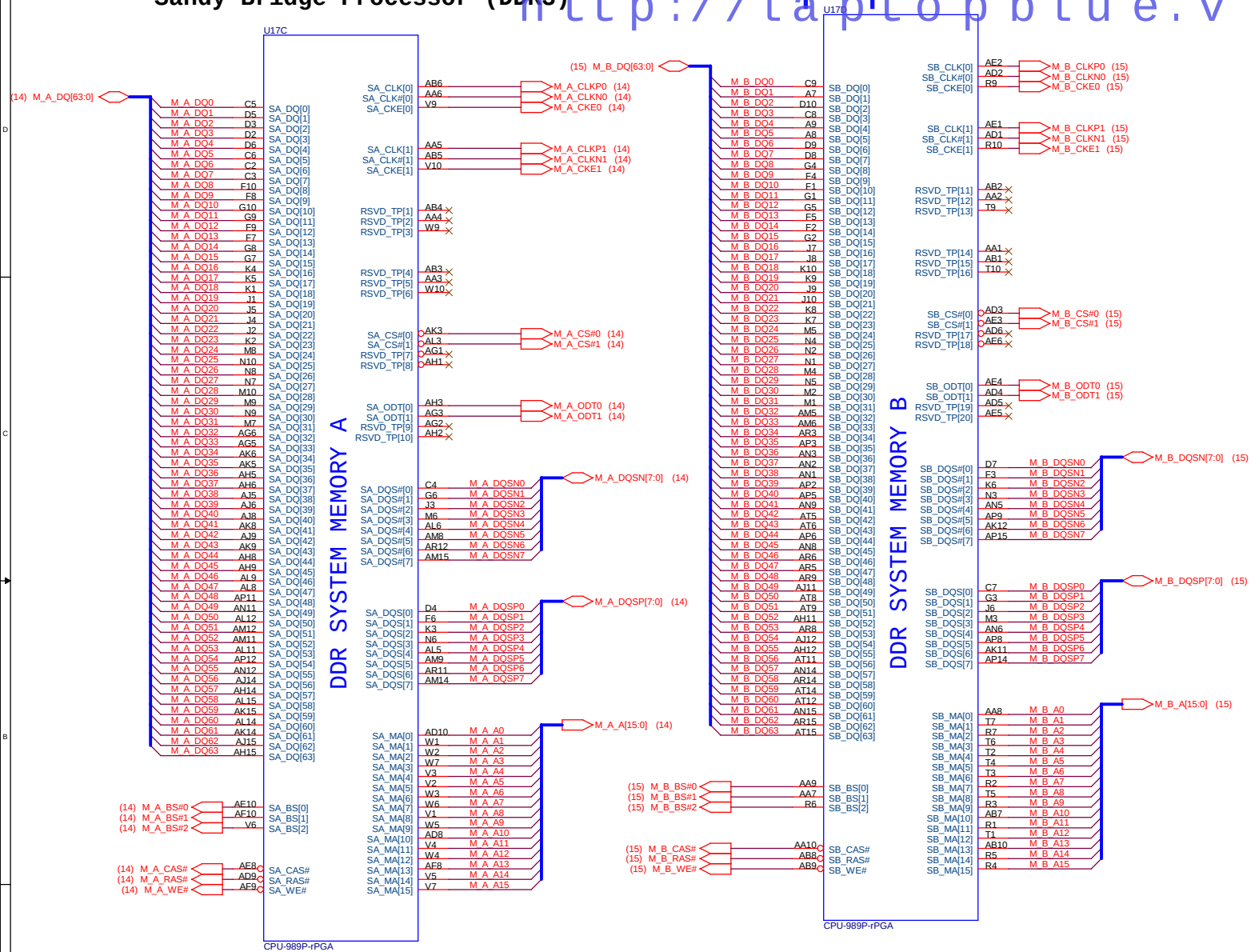
P19-- R345 change to 48.7K.
Reason : Change thermal sensor temperature to 55 degree.
Possible Risk: No.

P19-- R208 change to 27.4K for UMA.
Reason : Change thermal sensor temperature to 82 degree.
Possible Risk: No.

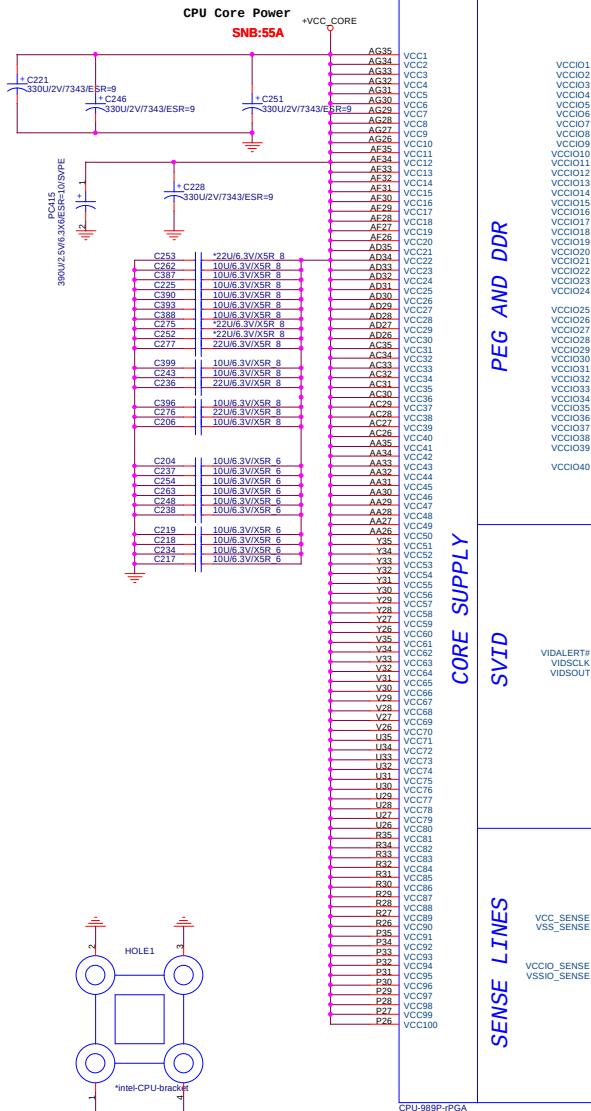
HK1_MB_SCH_PVT_002

P16-- KR27 change value from 10K to 4.7K.
P33-- PD7 change value from uClamp3301D to CDS0D323-T03C.
Reason : for Battery ESD protect.
Possible Risk: No.

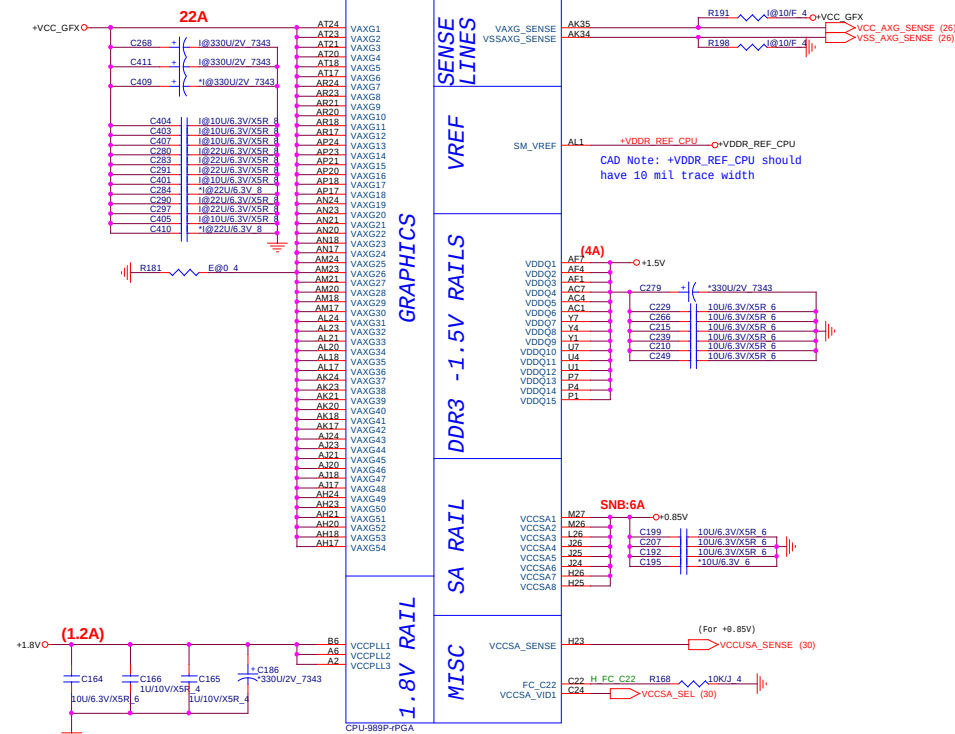




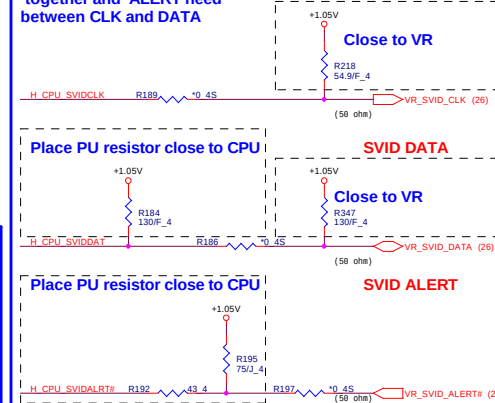
POWER



POWER



Layout note: need routing together and ALERT needs to be between CLK and DATA



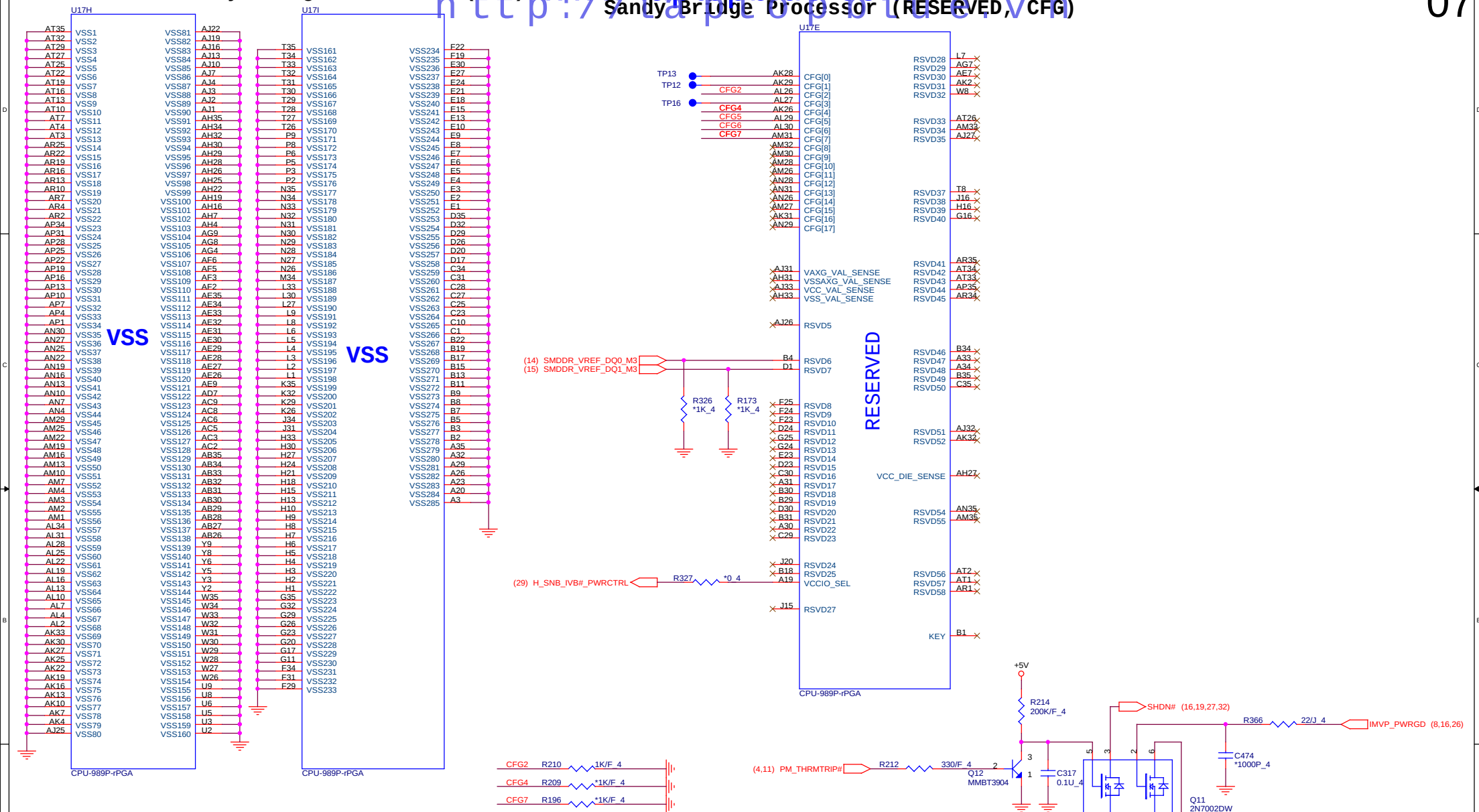
1.Level 1 Environment-related Substances Should Never be Used

2. Recycled Resin and Coated Wire should be procured from Green Partner

Sandy Bridge Processor (GND)

Sandy Bridge Processor (RESERVED,CFG)

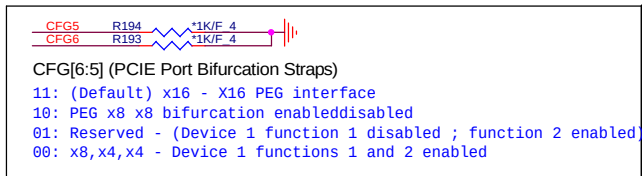
07



Processor Strapping

The CFG signals have a default value of '1' if not terminated on the board.

1	0
CFG2 (PEG Static Lane Reversal)	Normal Operation
CFG4 (DP Presence Strap)	Disable; No physical DP attached to eDP
CFG7 (PEG Defer Training)	PEG train immediately following xxRESETB de assertion





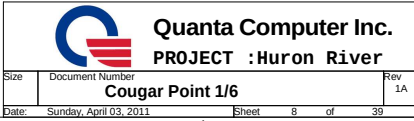
Quanta Computer Inc.
PROJECT : Huron River

Size	Document Number	Rev
	Sandy Bridge 4/4	1A

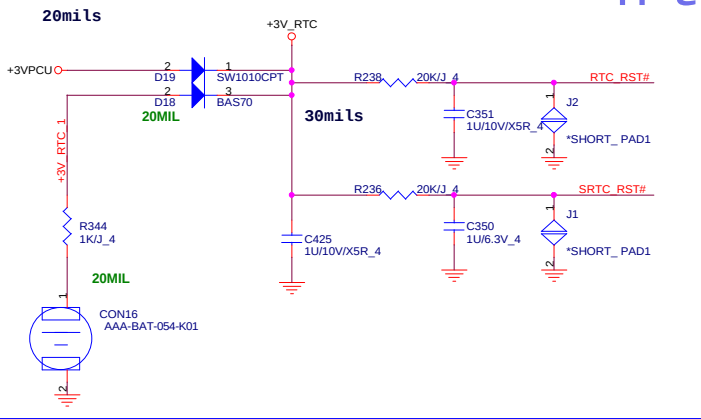
1. Level 1 Environment-related Substances Should Never be Used.

2. Recycled Resin and Coated Wire should be procured from Green Partners.

Date: Monday, February 21, 2011Sheet 7 of 39

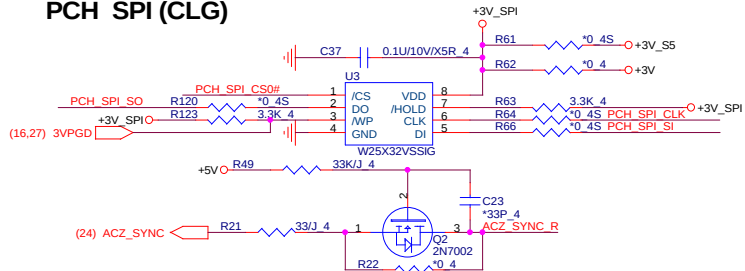


RTC Circuitry(RTC)



MX25L3205DM2I-12G: AKE39P0Z00
W25X32VSSIG: AKE39ZP0N00

PCH SPI (CLG)

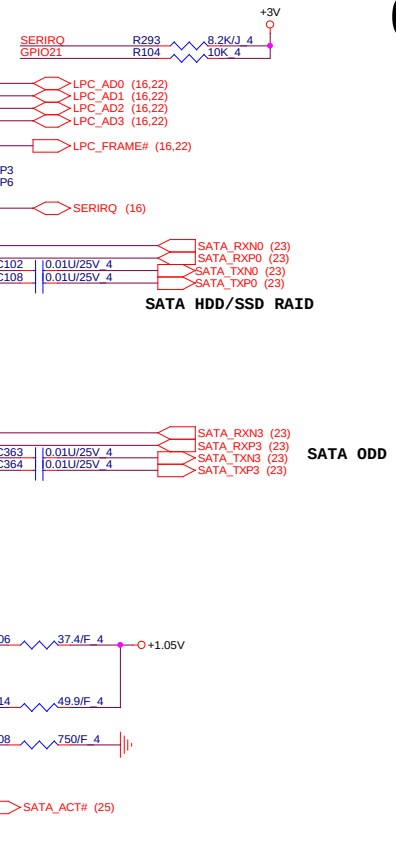
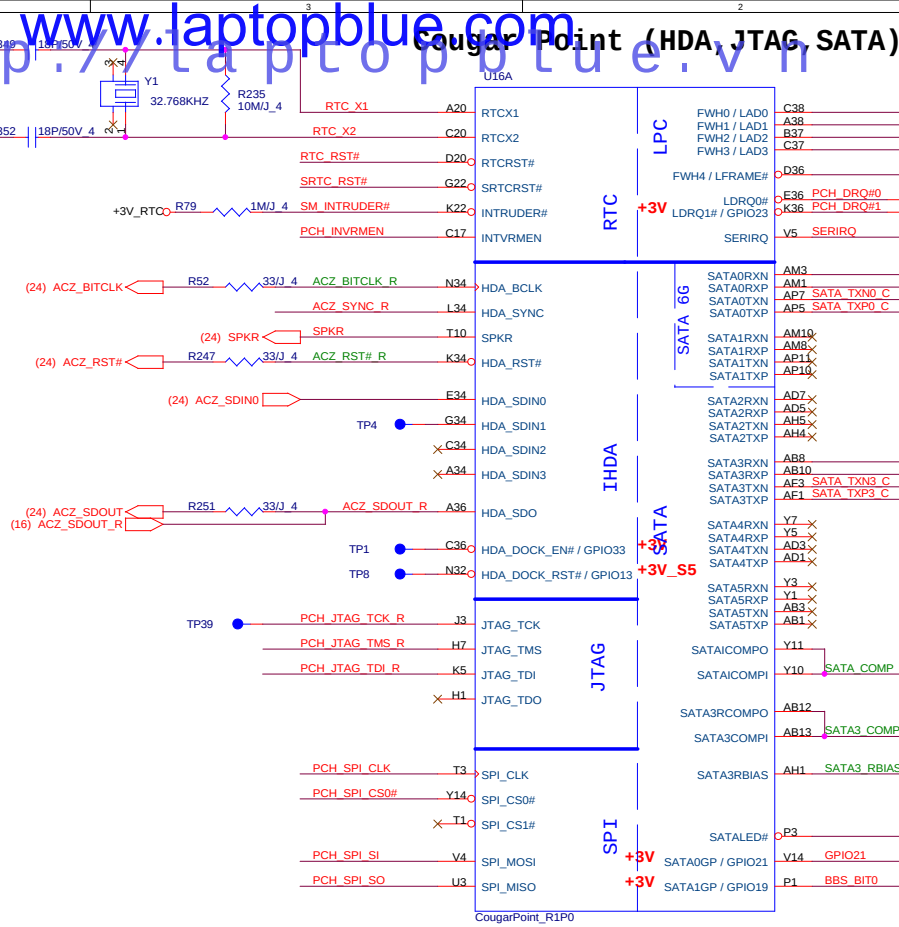


(16) F_CS0#_PCH	R121	*0.4S	PCH_SPI_CS0#
(16) F_SD1_PCH	R67	*0.4S	PCH_SPI_SO
(16) SCK_PCH	R65	*0.4S	PCH_SPI_CLK
(16) SD0_PCH	R122	*0.4S	PCH_SPI_SI

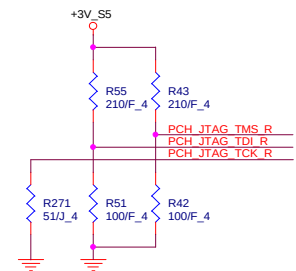
For NPCE795L Using

PCH Strap Table

Pin Name	Strap description	Sampled	Configuration	HK1/HK2 note									
SPKR	No reboot mode setting	PWROK	0 = Default (weak pull-down 20K) 1 = Setting to No-Reboot mode										
GNT3# / GPIO55	Top-Block Swap Override	PWROK	0 = "top-block swap" mode 1 = Default (weak pull-up 20K)										
INTVRMEN	Integrated 1.05V VRM enable	ALWAYS	Should be always pull-up										
GNT1# / GPIO51	Boot BIOS Selection 1 [bit-1]	PWROK	<table border="1"><thead><tr><th>GNT1#</th><th>GNT0#</th><th>Boot Location</th></tr></thead><tbody><tr><td>1</td><td>1</td><td>SPI *</td></tr><tr><td>0</td><td>0</td><td>LPC</td></tr></tbody></table>	GNT1#	GNT0#	Boot Location	1	1	SPI *	0	0	LPC	Default weak pull-up on GNT0/1# [Need external pull-down for LPC BIOS]
GNT1#	GNT0#	Boot Location											
1	1	SPI *											
0	0	LPC											
GPIO19	Boot BIOS Selection 0 [bit-0]	PWROK											
HDA_SDO	Flash Descriptor Security	PWROK	0 = Default (weak pull-down 20K) 1 = Enabled										
DF_TVS	DMI/FDI Termination voltage	PWROK	0 = Set to Vss 1 = Set to Vcc (weak pull-down 20K)										
GPIO28	On-die PLL Voltage Regulator	RSMRST#	0 = Disable 1 = Enable (Default)										
HDA_SYNC	On-Die PLL VR Voltage Select	RSMRST	0 = Support by 1.8V (weak pull-down) 1 = Support by 1.5V										

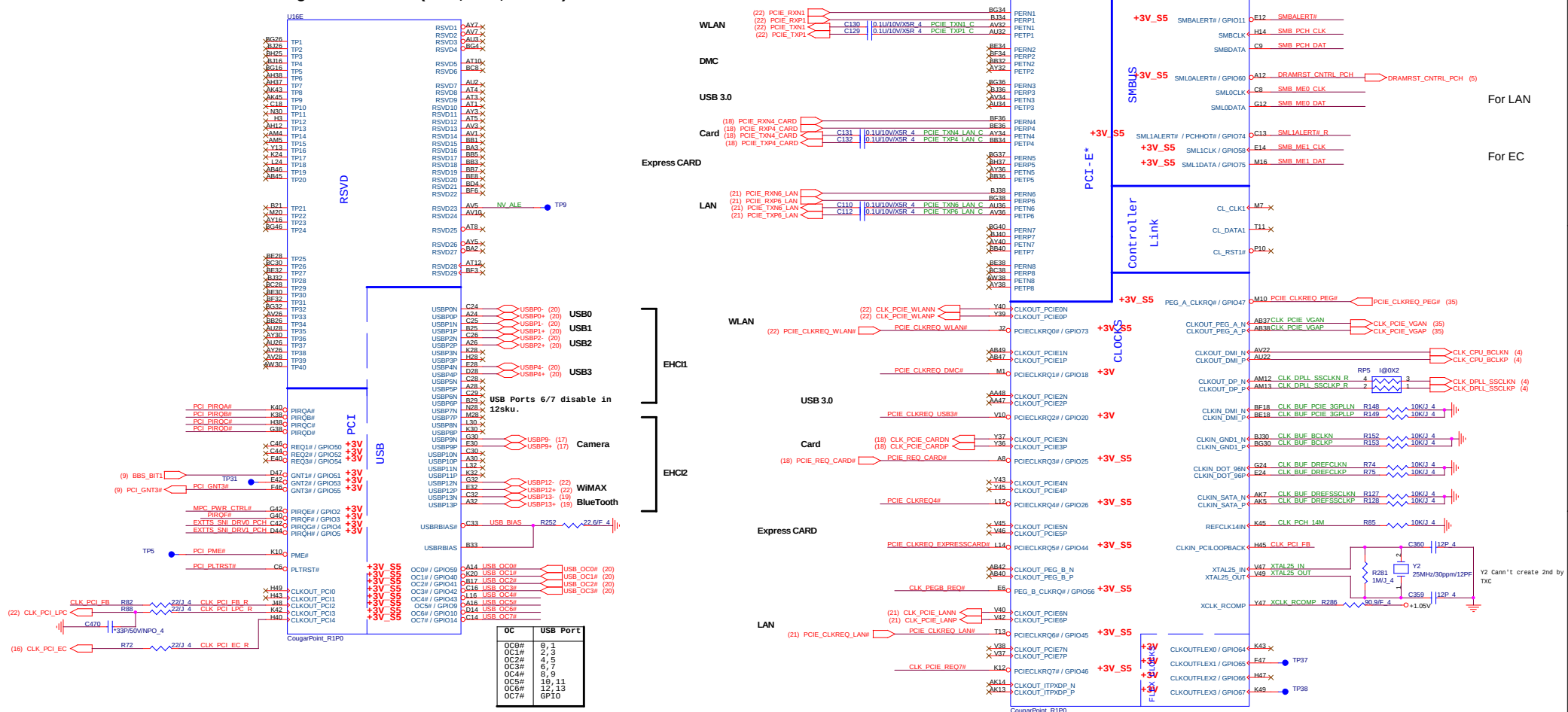


PCH JTAG Debug (CLG)

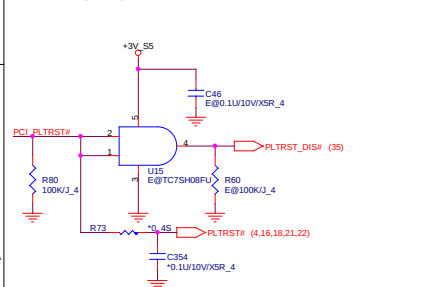


Cougar Point-M (PCI, USB, NVRAM)

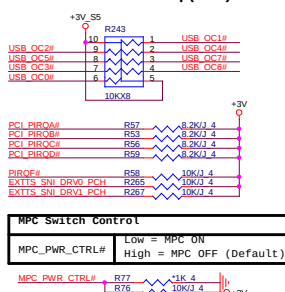
Cougar Point-M (PCI-E, SMBUS, CLK)



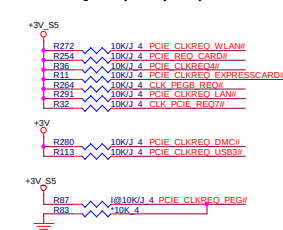
PLTRST#(CLG)



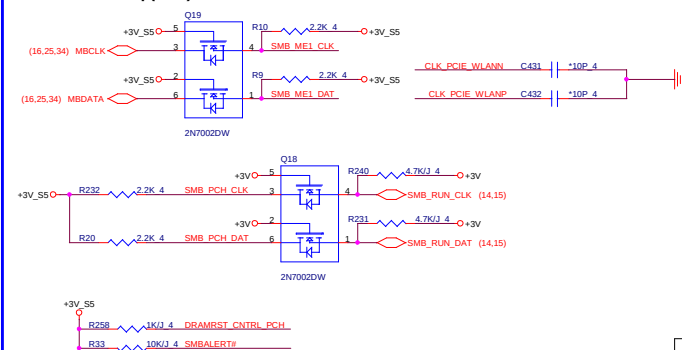
PCI/USBOC# Pull-up(CLG)

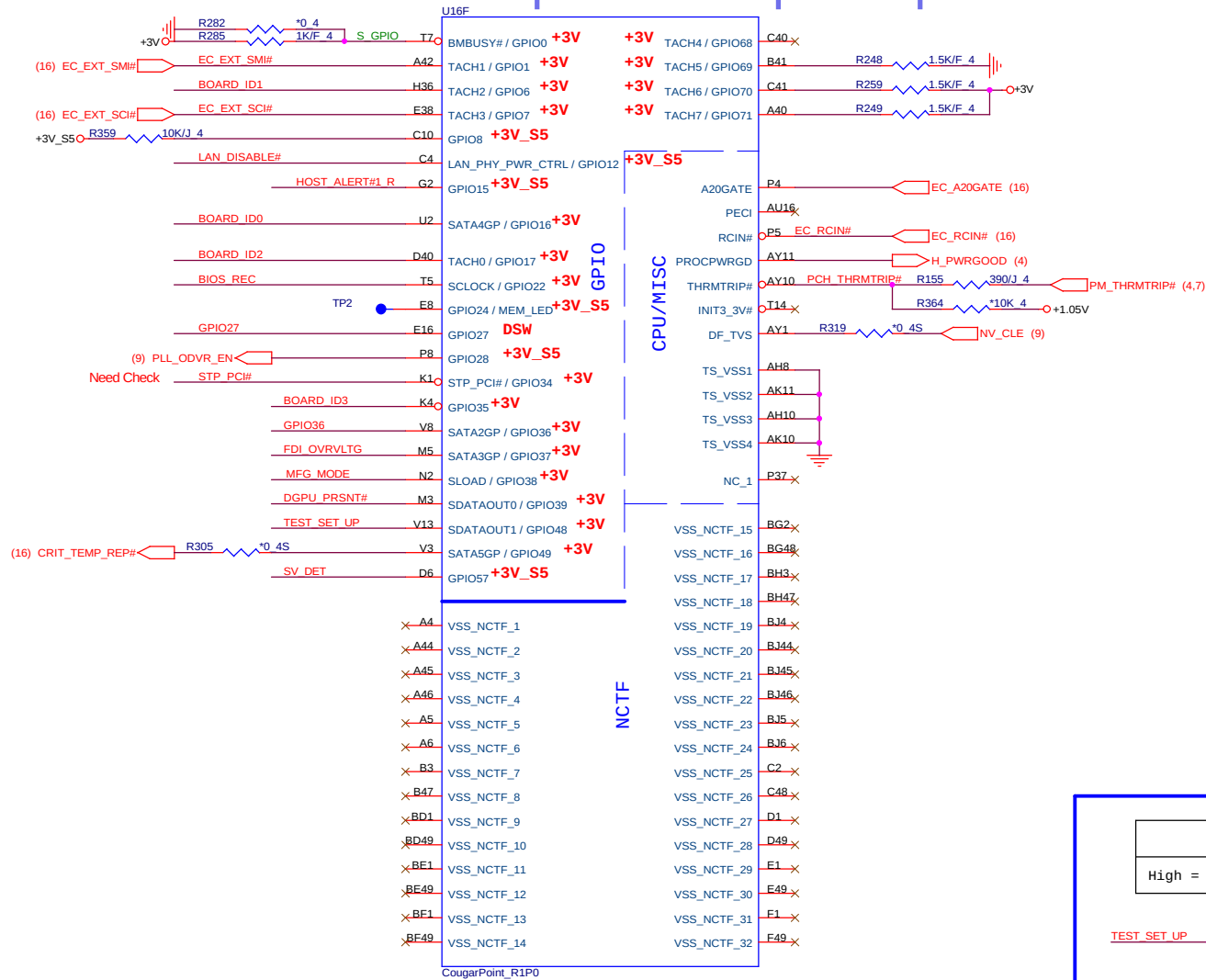


CLK_REQ/Strap Pin(CLG)

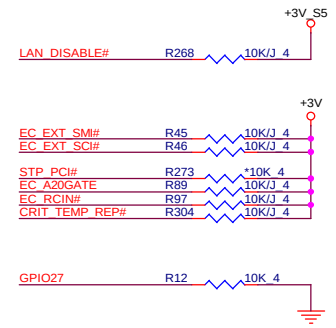


SMBus/Pull-up(CLG)





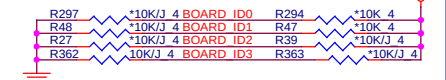
GPIO Pull-up/Pull-down(CLG)



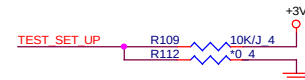
Board ID0 (N12M/N12P)	N12M	N12P
R294	Stuff	No Stuff
R297	No Stuff	Stuff

Board ID1 (VRAM Vendor)	Samaung	Hynix
R47	Stuff	No Stuff
R48	No Stuff	Stuff

Board ID2 (VRAM 1G/512M)	1G	512M
R39	Stuff	No Stuff
R27	No Stuff	Stuff



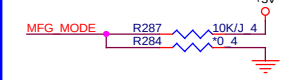
SV_SET_UP
High = Strong (Default)



HOST_ALERT#1_R
High = Strong (Default)

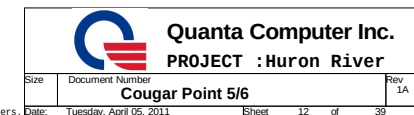
Intel ME Crypto Transport Layer Security (TLS) cipher suite
Low = Disable (Default)
High = Enable

MFG - TEST



PCBA SKU	Discrete	UMA
R277	Stuff	No Stuff
R275	No Stuff	Stuff

Q	Quanta Computer Inc.
P	PROJECT : Huron River



PCH6(CLG)

IBEX PEAK-M (GND)

<http://laptopblue.vn>

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U16H		
H5	VSS[0]	
AA17	VSS[1]	VSS[80]
AA2	VSS[2]	VSS[81]
AA3	VSS[3]	VSS[82]
AA33	VSS[4]	VSS[83]
AA34	VSS[5]	VSS[84]
AB11	VSS[6]	VSS[85]
AB14	VSS[7]	VSS[86]
AB38	VSS[8]	VSS[87]
AB4	VSS[9]	VSS[88]
AB43	VSS[10]	VSS[89]
AB5	VSS[11]	VSS[90]
AB7	VSS[12]	VSS[91]
AC19	VSS[13]	VSS[92]
AC2	VSS[14]	VSS[93]
AC21	VSS[15]	VSS[94]
AC24	VSS[16]	VSS[95]
AC33	VSS[17]	VSS[96]
AC34	VSS[18]	VSS[97]
AC46	VSS[19]	VSS[98]
AD10	VSS[20]	VSS[99]
AD11	VSS[21]	VSS[100]
AD12	VSS[22]	VSS[101]
AD13	VSS[23]	VSS[102]
AD19	VSS[24]	VSS[103]
AD24	VSS[25]	VSS[104]
AD26	VSS[26]	VSS[105]
AD27	VSS[27]	VSS[106]
AD33	VSS[28]	VSS[107]
AD34	VSS[29]	VSS[108]
AD36	VSS[30]	VSS[109]
AD37	VSS[31]	VSS[110]
AD38	VSS[32]	VSS[111]
AD39	VSS[33]	VSS[112]
AD4	VSS[34]	VSS[113]
AD40	VSS[35]	VSS[114]
AD42	VSS[36]	VSS[115]
AD43	VSS[37]	VSS[116]
AD45	VSS[38]	VSS[117]
AD46	VSS[39]	VSS[118]
AD8	VSS[40]	VSS[119]
AE2	VSS[41]	VSS[120]
AE3	VSS[42]	VSS[121]
AF10	VSS[43]	VSS[122]
AF12	VSS[44]	VSS[123]
AD14	VSS[45]	VSS[124]
AD16	VSS[46]	VSS[125]
AF16	VSS[47]	VSS[126]
AF19	VSS[48]	VSS[127]
AF24	VSS[49]	VSS[128]
AF26	VSS[50]	VSS[129]
AF27	VSS[51]	VSS[130]
AF29	VSS[52]	VSS[131]
AF31	VSS[53]	VSS[132]
AF38	VSS[54]	VSS[133]
AF4	VSS[55]	VSS[134]
AF42	VSS[56]	VSS[135]
AF46	VSS[57]	VSS[136]
AF5	VSS[58]	VSS[137]
AF7	VSS[59]	VSS[138]
AF8	VSS[60]	VSS[139]
AG19	VSS[61]	VSS[140]
AG2	VSS[62]	VSS[141]
AG31	VSS[63]	VSS[142]
AG48	VSS[64]	VSS[143]
AH11	VSS[65]	VSS[144]
AH3	VSS[66]	VSS[145]
AH36	VSS[67]	VSS[146]
AH39	VSS[68]	VSS[147]
AH40	VSS[69]	VSS[148]
AH42	VSS[70]	VSS[149]
AH46	VSS[71]	VSS[150]
AH7	VSS[72]	VSS[151]
AJ19	VSS[73]	VSS[152]
AJ21	VSS[74]	VSS[153]
AJ24	VSS[75]	VSS[154]
AJ33	VSS[76]	VSS[155]
AJ34	VSS[77]	VSS[156]
AK12	VSS[78]	VSS[157]
AK3	VSS[79]	VSS[158]

CougarPoint_R1P0



AY4	VSS[159]	H46
AY42	VSS[160]	K18
AY46	VSS[161]	K26
AY8	VSS[162]	K39
B11	VSS[163]	K46
B15	VSS[164]	K7
B19	VSS[165]	L18
B23	VSS[166]	L2
B27	VSS[167]	L20
B31	VSS[168]	L26
B35	VSS[169]	L28
B39	VSS[170]	L36
B7	VSS[171]	L48
F45	VSS[172]	M12
BB12	VSS[173]	P16
BB16	VSS[174]	M18
BB20	VSS[175]	M22
BB22	VSS[176]	M24
BB24	VSS[177]	M30
BB28	VSS[178]	M32
BB30	VSS[179]	M34
BB38	VSS[180]	M38
BB4	VSS[181]	M4
BB46	VSS[182]	M42
BC14	VSS[183]	M46
BC18	VSS[184]	M8
BC2	VSS[185]	N18
BC22	VSS[186]	P30
BC26	VSS[187]	P47
BC32	VSS[188]	P11
BC34	VSS[189]	P18
BC36	VSS[190]	T33
BC40	VSS[191]	P40
RC42	VSS[192]	P43
BC48	VSS[193]	P47
BD46	VSS[194]	R7
BD5	VSS[195]	R2
BE22	VSS[196]	R48
BE26	VSS[197]	T12
BE40	VSS[198]	T31
BE10	VSS[199]	T37
BE12	VSS[200]	T4
BF16	VSS[201]	V34
BF20	VSS[202]	T46
BF22	VSS[203]	T47
BF24	VSS[204]	T8
BE26	VSS[205]	V11
AT18	VSS[206]	V17
BD3	VSS[207]	V26
BF30	VSS[208]	V27
BF38	VSS[209]	V29
BF40	VSS[210]	V31
BF8	VSS[211]	V36
BG17	VSS[212]	V39
BG21	VSS[213]	V43
BG33	VSS[214]	V7
BG44	VSS[215]	W17
BG8	VSS[216]	W19
BH11	VSS[217]	W2
AU30	VSS[218]	W27
BH17	VSS[219]	W48
BH19	VSS[220]	Y12
H10	VSS[221]	Y38
BH27	VSS[222]	Y4
BH31	VSS[223]	Y42
AV4	VSS[224]	Y46
BH33	VSS[225]	Y8
BH35	VSS[226]	BG29
BH39	VSS[227]	N24
BH43	VSS[228]	AJ3
BH7	VSS[229]	AD47
D3	VSS[230]	B43
D12	VSS[231]	BE10
D16	VSS[232]	BG41
D18	VSS[233]	G14
D22	VSS[234]	H16
D24	VSS[235]	T36
D26	VSS[236]	BG22
D30	VSS[237]	BG24
D32	VSS[238]	C22
D34	VSS[239]	AP13
D38	VSS[240]	M14
D42	VSS[241]	AP3
D8	VSS[242]	AP1
E18	VSS[243]	BE16
E26	VSS[244]	BC16
G18	VSS[245]	BG28
G20	VSS[246]	BJ28
G26	VSS[247]	
G28	VSS[248]	
G36	VSS[249]	
G48	VSS[250]	
H12	VSS[251]	
H18	VSS[252]	
H22	VSS[253]	
H24	VSS[254]	
H26	VSS[255]	
H30	VSS[256]	
H32	VSS[257]	
H34	VSS[258]	
F3		

CougarPoint_R1P0



VSS[259]	K18
VSS[260]	K26
VSS[261]	K39
VSS[262]	K46
VSS[263]	K7
VSS[264]	L18
VSS[265]	L2
VSS[266]	L20
VSS[267]	L26
VSS[268]	L28
VSS[269]	L36
VSS[270]	L48
VSS[271]	M12
VSS[272]	P16
VSS[273]	M18
VSS[274]	M22
VSS[275]	M24
VSS[276]	M30
VSS[277]	M32
VSS[278]	M34
VSS[279]	M38
VSS[280]	M4
VSS[281]	M42
VSS[282]	M46
VSS[283]	M8
VSS[284]	N18
VSS[285]	P30
VSS[286]	P47
VSS[287]	P11
VSS[288]	P18
VSS[289]	T33
VSS[290]	P40
VSS[291]	P43
VSS[292]	P47
VSS[293]	R7
VSS[294]	R2
VSS[295]	R48
VSS[296]	T12
VSS[297]	T31
VSS[298]	T37
VSS[299]	T4
VSS[300]	V34
VSS[301]	T46
VSS[302]	T47
VSS[303]	T8
VSS[304]	V11
VSS[305]	V17
VSS[306]	V26
VSS[307]	V27
VSS[308]	V29
VSS[309]	V31
VSS[310]	V36
VSS[311]	V39
VSS[312]	V43
VSS[313]	V7
VSS[314]	W17
VSS[315]	W19
VSS[316]	W2
VSS[317]	W27
VSS[318]	W48
VSS[319]	Y12
VSS[320]	Y38
VSS[321]	Y4
VSS[322]	Y42
VSS[323]	Y46
VSS[324]	Y8
VSS[325]	BG29
VSS[326]	N24
VSS[327]	AJ3
VSS[328]	AD47
VSS[329]	B43
VSS[330]	BE10
VSS[331]	BG41
VSS[332]	G14
VSS[333]	H16
VSS[334]	T36
VSS[335]	BG22
VSS[336]	BG24
VSS[337]	C22
VSS[338]	AP13
VSS[339]	M14
VSS[340]	AP3
VSS[341]	AP1
VSS[342]	BE16
VSS[343]	BC16
VSS[344]	BG28
VSS[345]	BJ28
VSS[346]	
VSS[347]	
VSS[348]	
VSS[349]	
VSS[350]	
VSS[351]	
VSS[352]	



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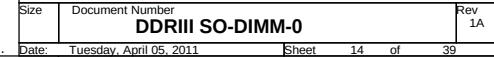
PROJECT : Huron River

Size	Document Number	Rev
	Cougar Point 6/6	1A
1		

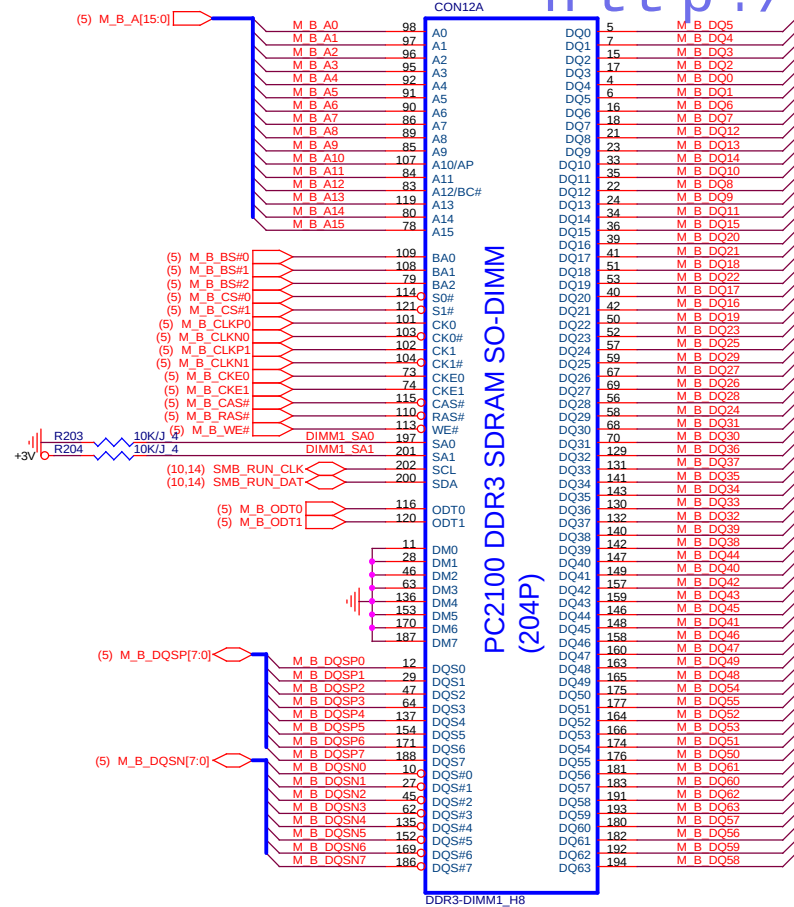
1.Level 1 Environment-related Substances Should Never be Used.

2.Recycled Resin and Coated Wire should be procured from Green Partners.

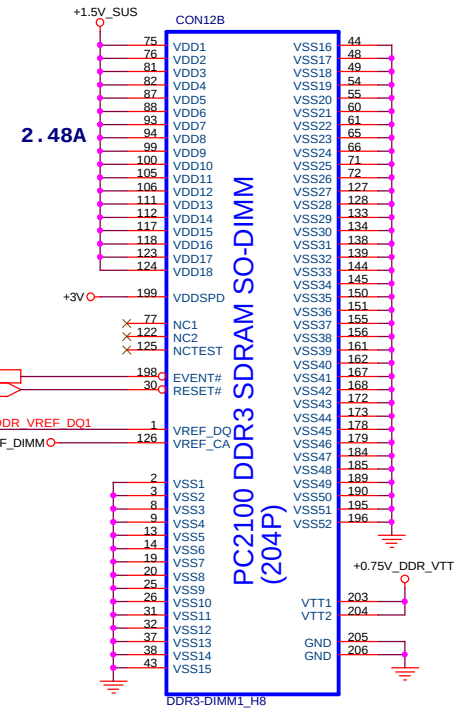
Date: Monday, February 21, 2011 Sheet 13 of 39



DDR_RVS(DDR)

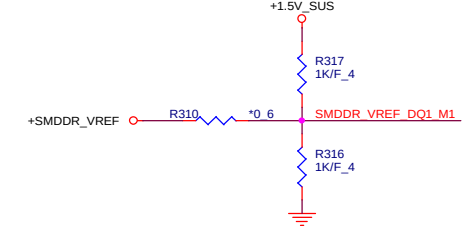


RUV Type

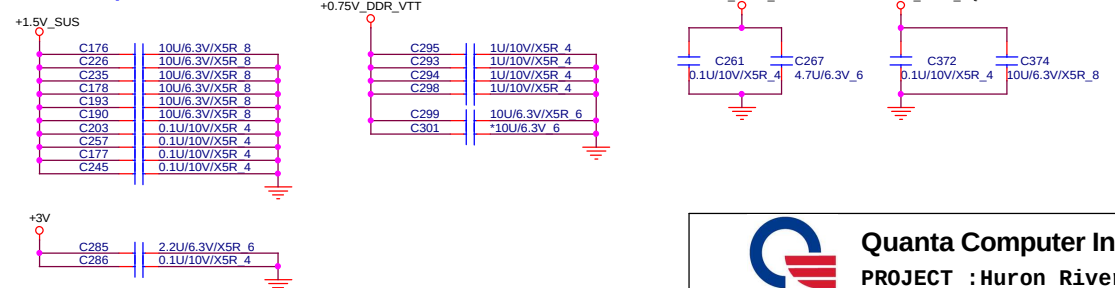


RUV Type

VREF DQ1 M1 Solution



Place these Caps near So-Dimm1.



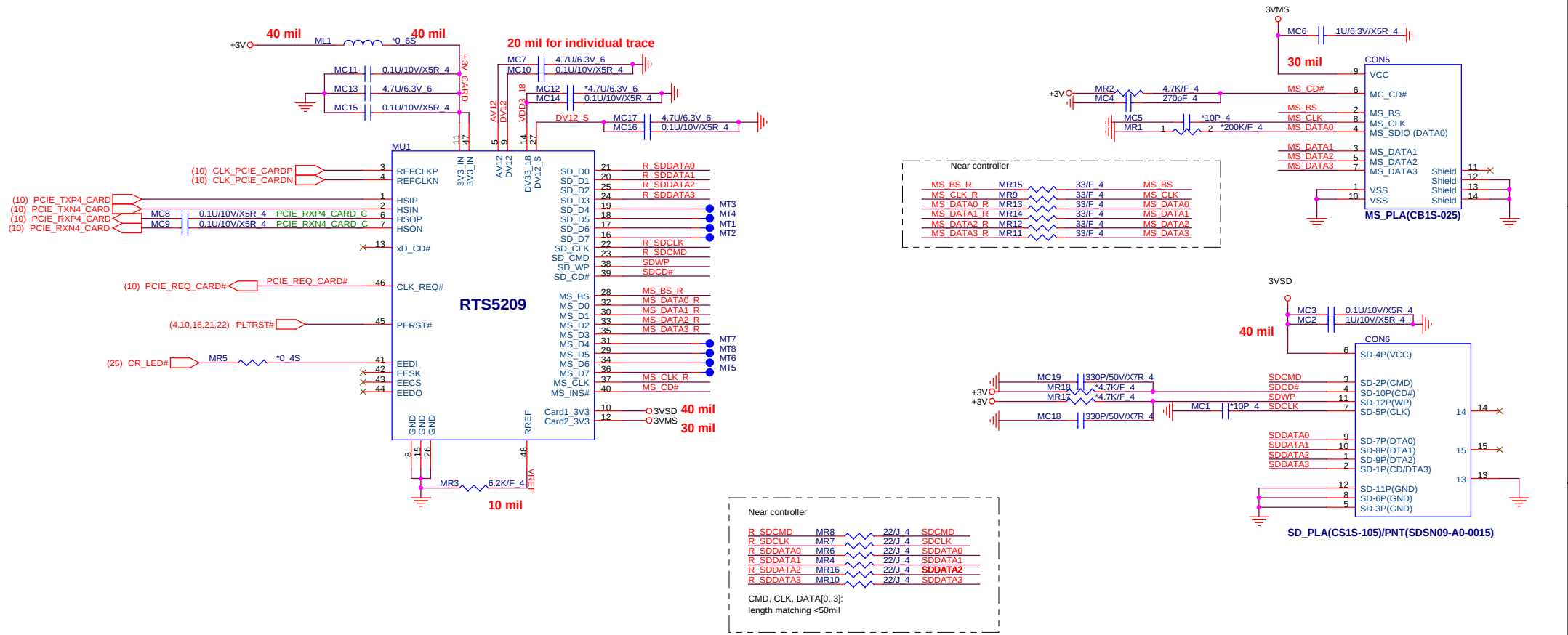
Quanta Computer Inc.
PROJECT : Huron River

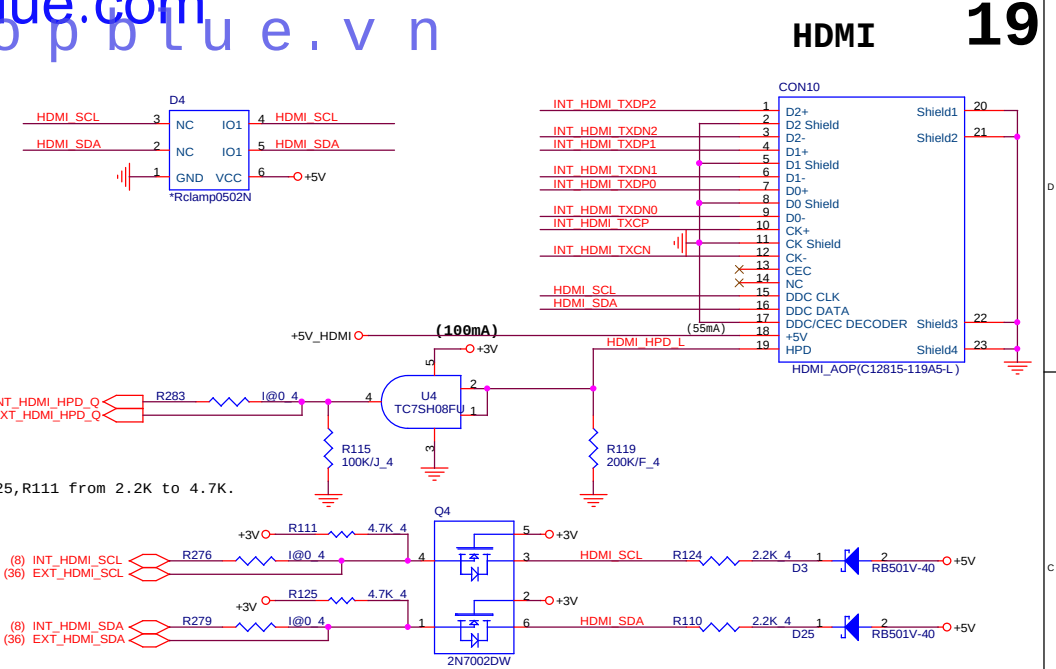
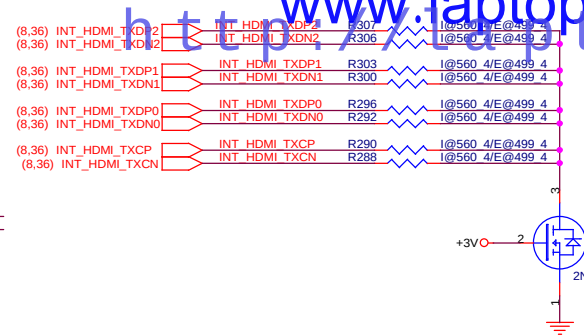
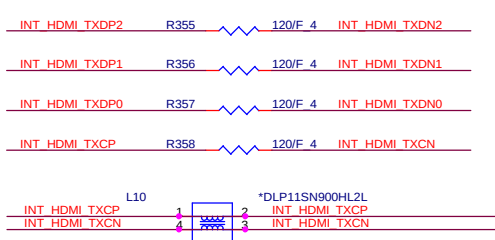
Size	Document Number	Rev
	DDR3 SO-DIMM-1	1A

1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Date: Tuesday, April 05, 2011 Sheet 15 of 39



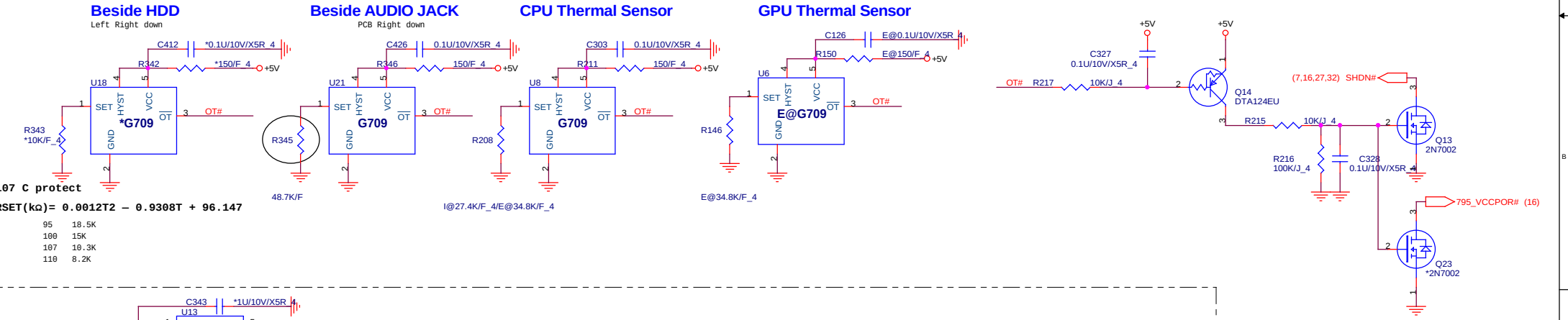




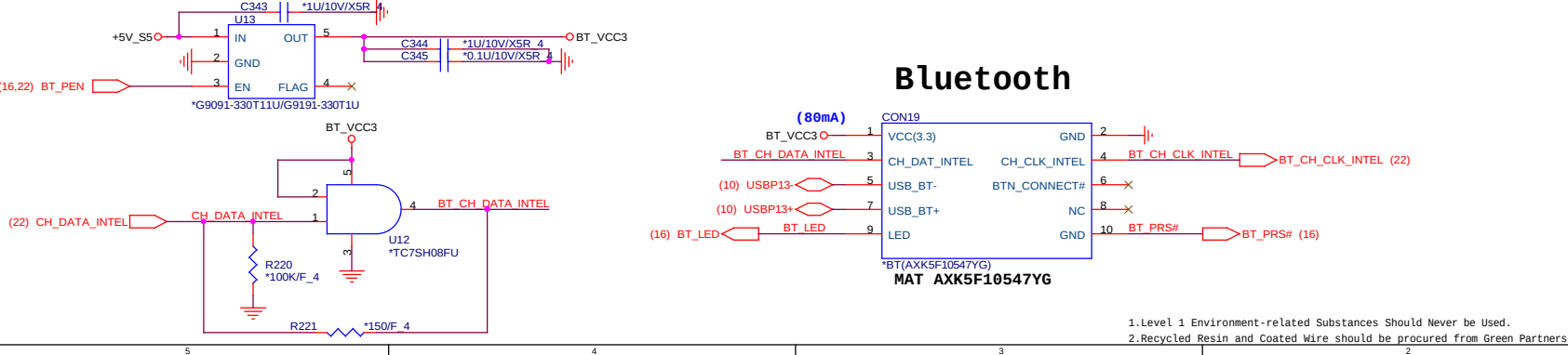
DIS SKU					
Location of IC	Temp	R-Set	Parts in BOM	Max	Min
Near CPU sensor temp	73	R208=34.59K	34.8K	73.2	72.2
Near GFX sensor temp	73	R146=34.59K	34.8K	73.2	72.2
Near AUDIO sensor temp	55	R345=48.58K	48.7K	55.5	54.2

UMA SKU					
Location of IC	Temp	R-Set	Parts in BOM	Max	Min
Near CPU sensor temp	82	R208=27.89K	27.4K	83.1	82.2
Near AUDIO sensor temp	55	R345=48.58K	48.7K	55.5	54.2

H/W Thermal Protect



Bluetooth

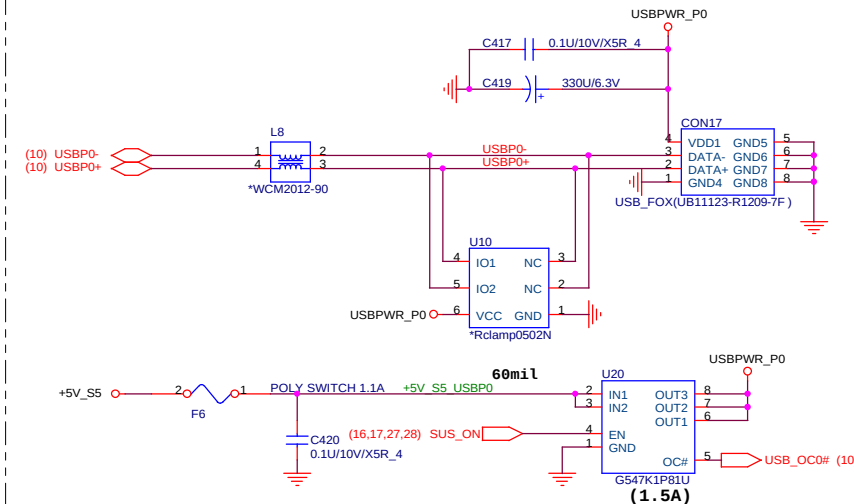
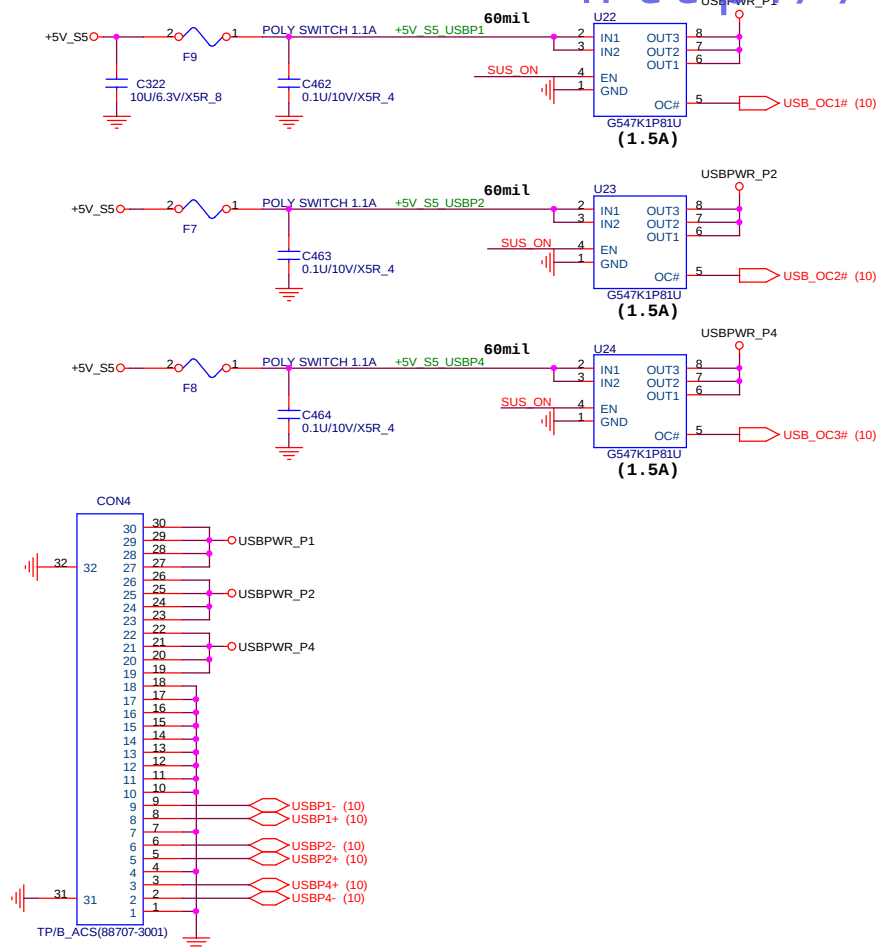


MB to USB board

http://laptopblue.vn

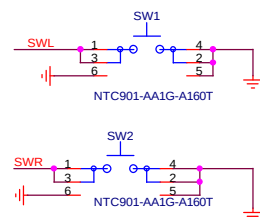
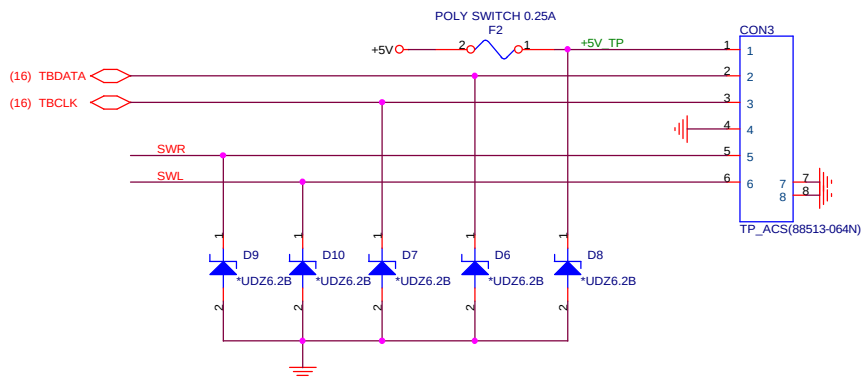
USB PORT 0

20

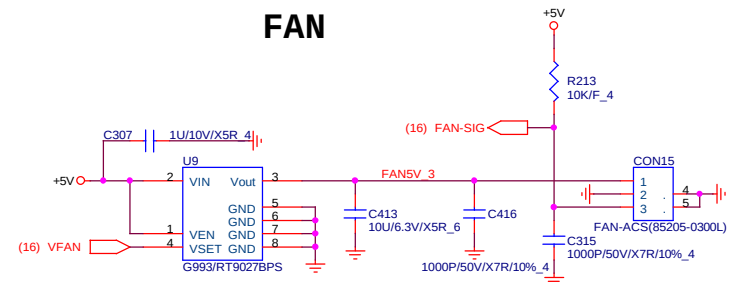


T/P Board to T/P

9/13 change F2 P/N from 0.12A to 0.25A



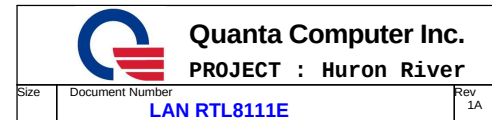
FAN

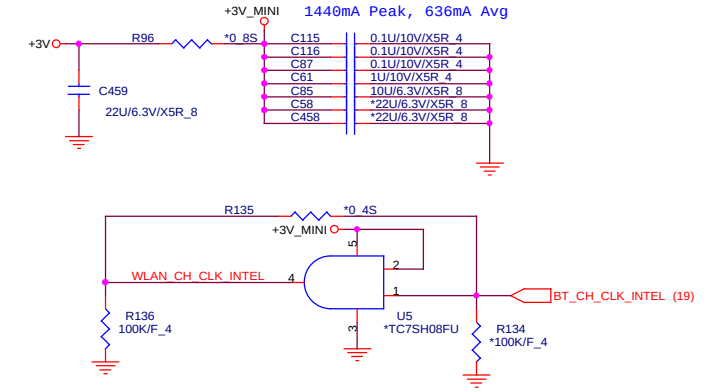
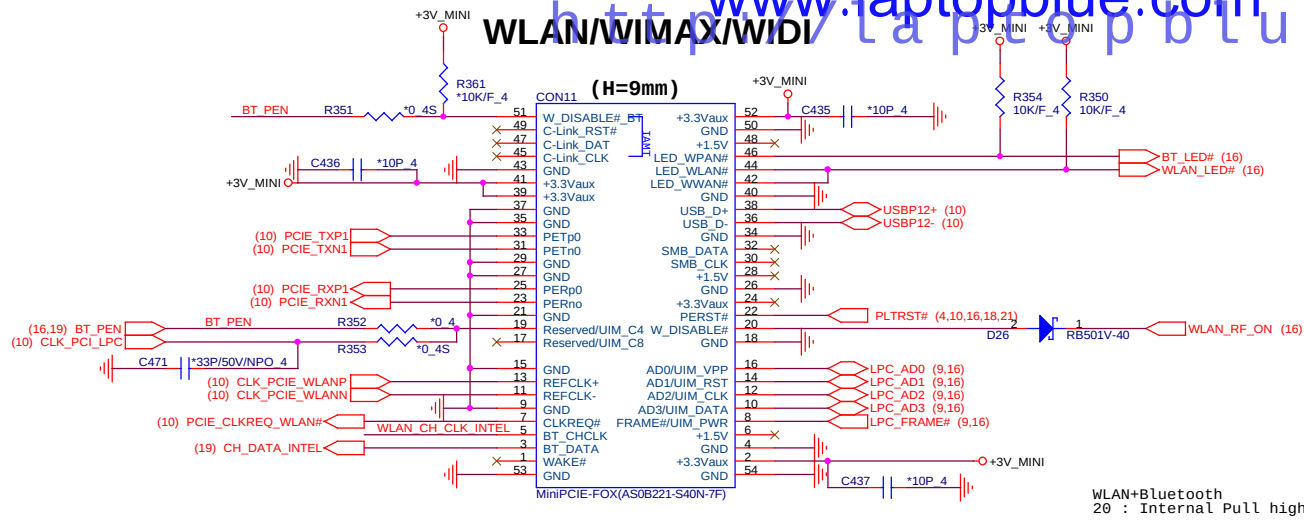


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Size	Document Number	Rev
	USB/TP/FAN	1A

1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.
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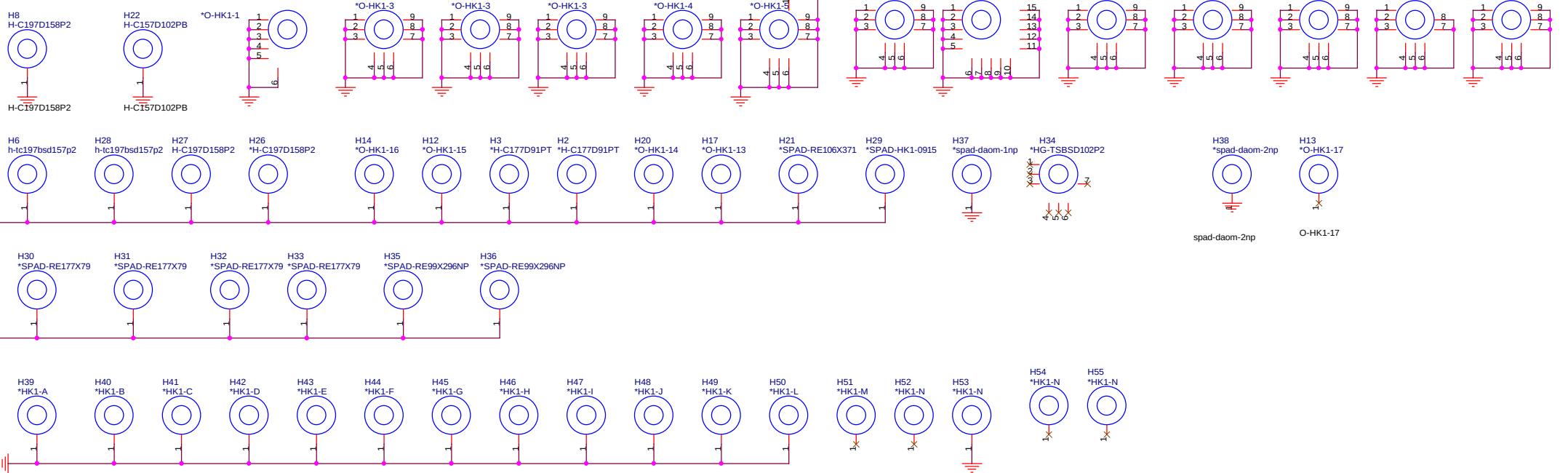




HOLE

WLAN

BT



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PROJECT : Huron River

Size	Document Number	Rev
	WLAN/WiMAX/WiDI	1A

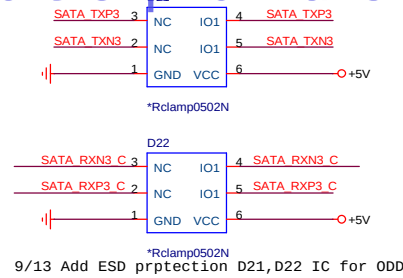
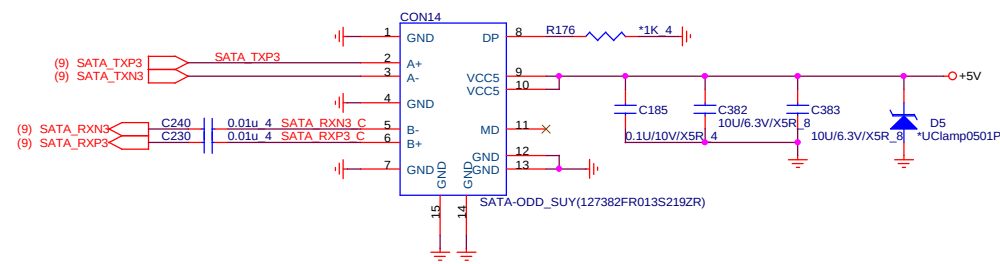
1.Level 1 Environment-related Substances Should Never be Used.

2.Recycled Resin and Coated Wire should be procured from Green Partners.

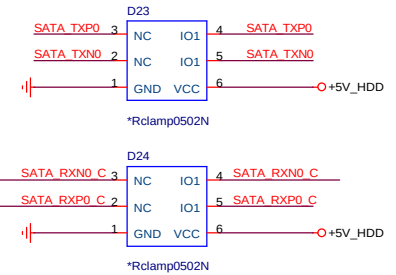
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ODD CONNECTOR

8/18 Change HDD 2nd source connector patt number

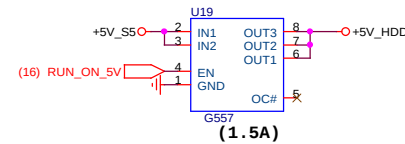
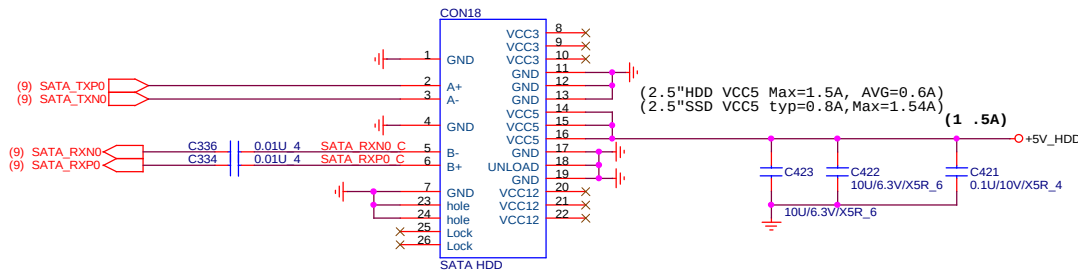


9/13 Add ESD prtection D21,D22 IC for ODD

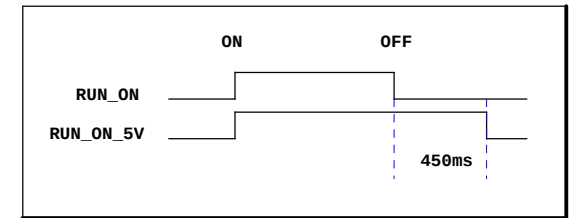


9/13 Add ESD prtection D23,D24 IC for HDD

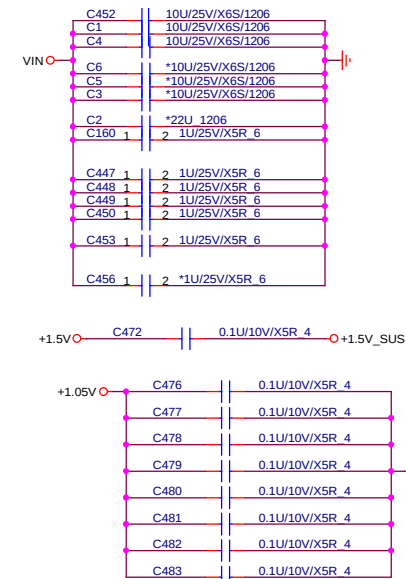
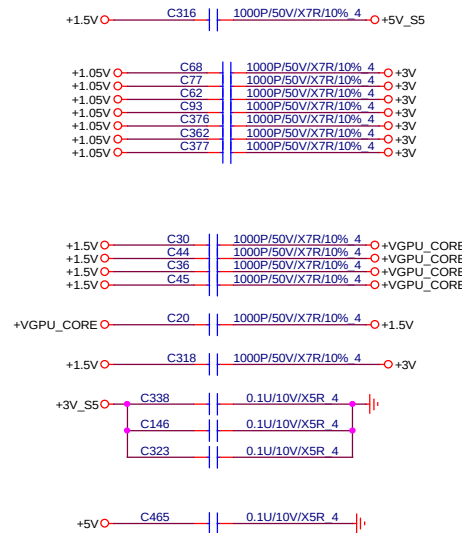
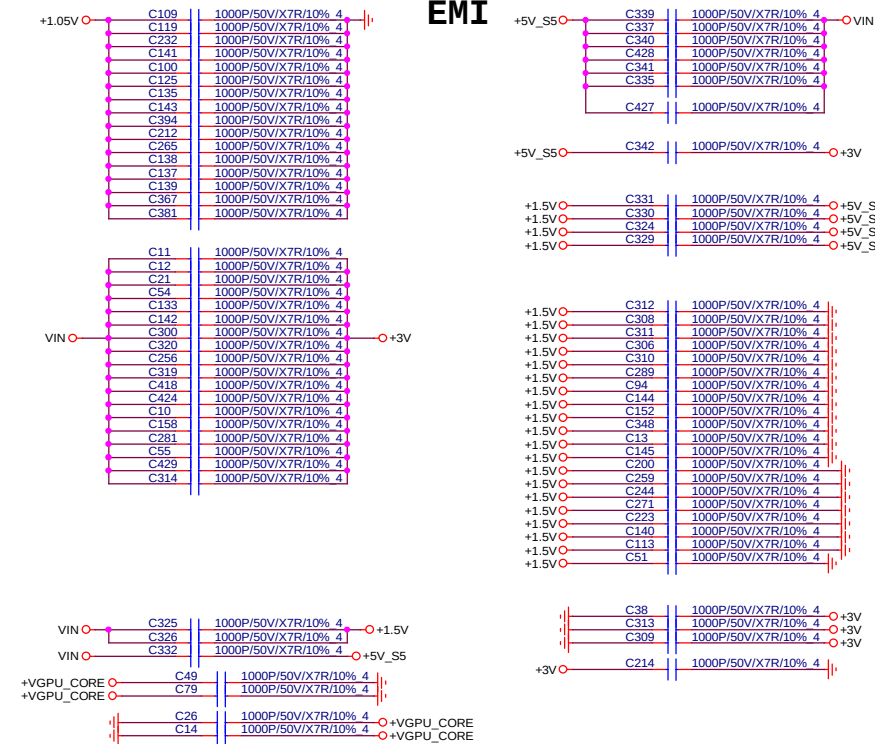
HDD CONNECTOR



1/18 Change U19 from G547 to G557.



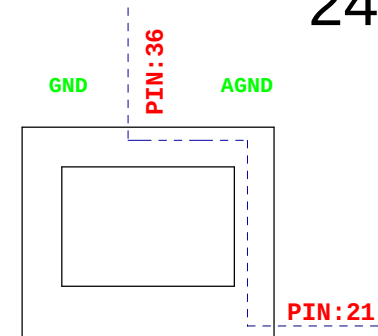
EMI



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Size	Document Number	Rev
	HDD/ODD	1A

1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.
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1/18 Change AL1~AL4 footprint from 0ohm to short pad



HP

MIC



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PROJECT : Huron River

Audio Codec CX20671

Rev
1.0

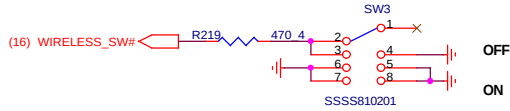
1. Level 1 Environment-related Substances Should Never be Used

2. Recycled Resin and Coated Wire should be procured from Green Partners.

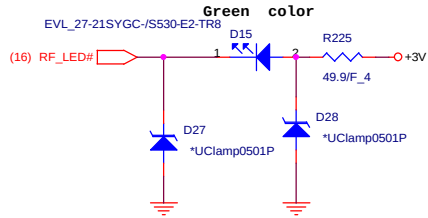
rs, Date: Tuesday, April 05, 2011

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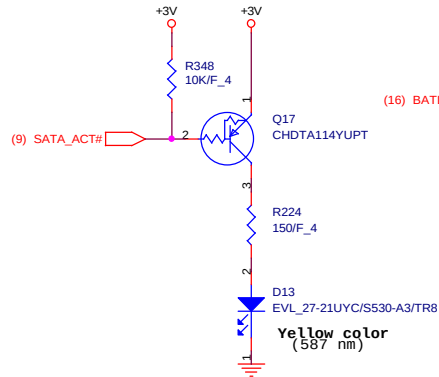
WIRELESS/Bluetooth SWITCH



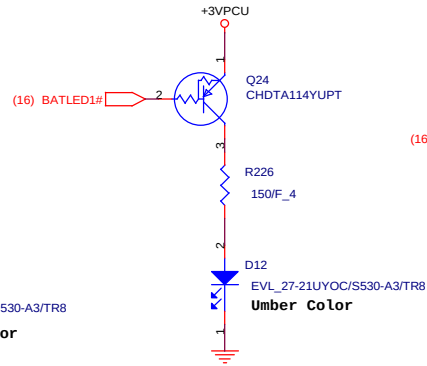
RF LED



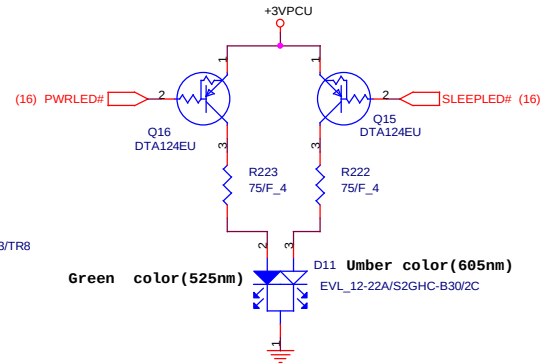
SATA LED



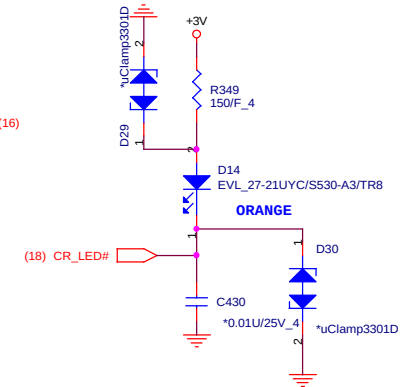
BATTERY LED



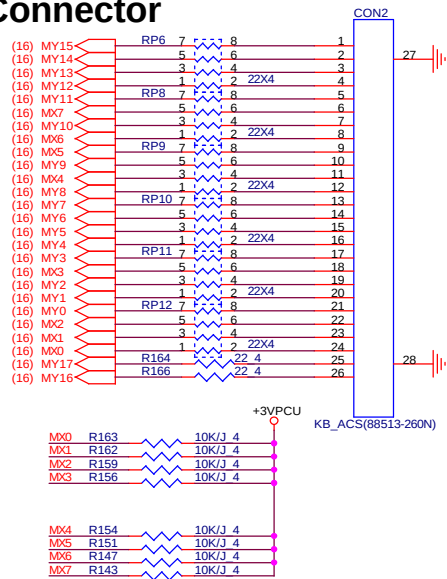
Power/Sleep LED



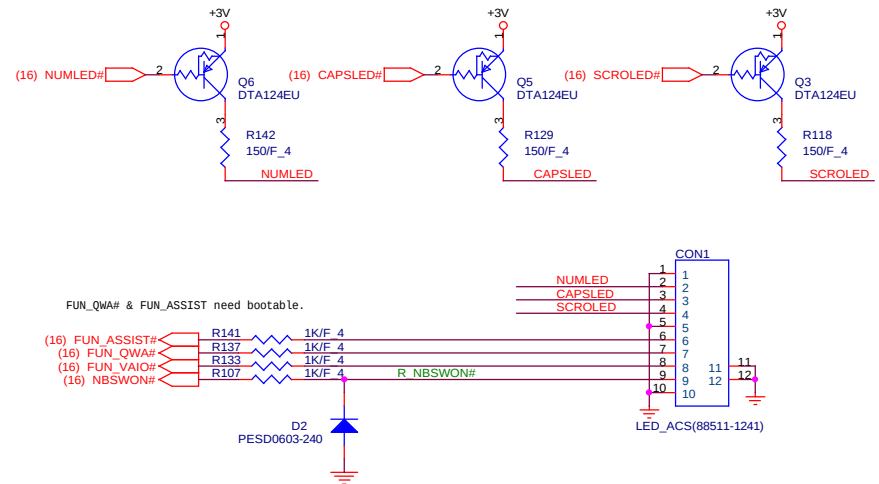
CARD LED

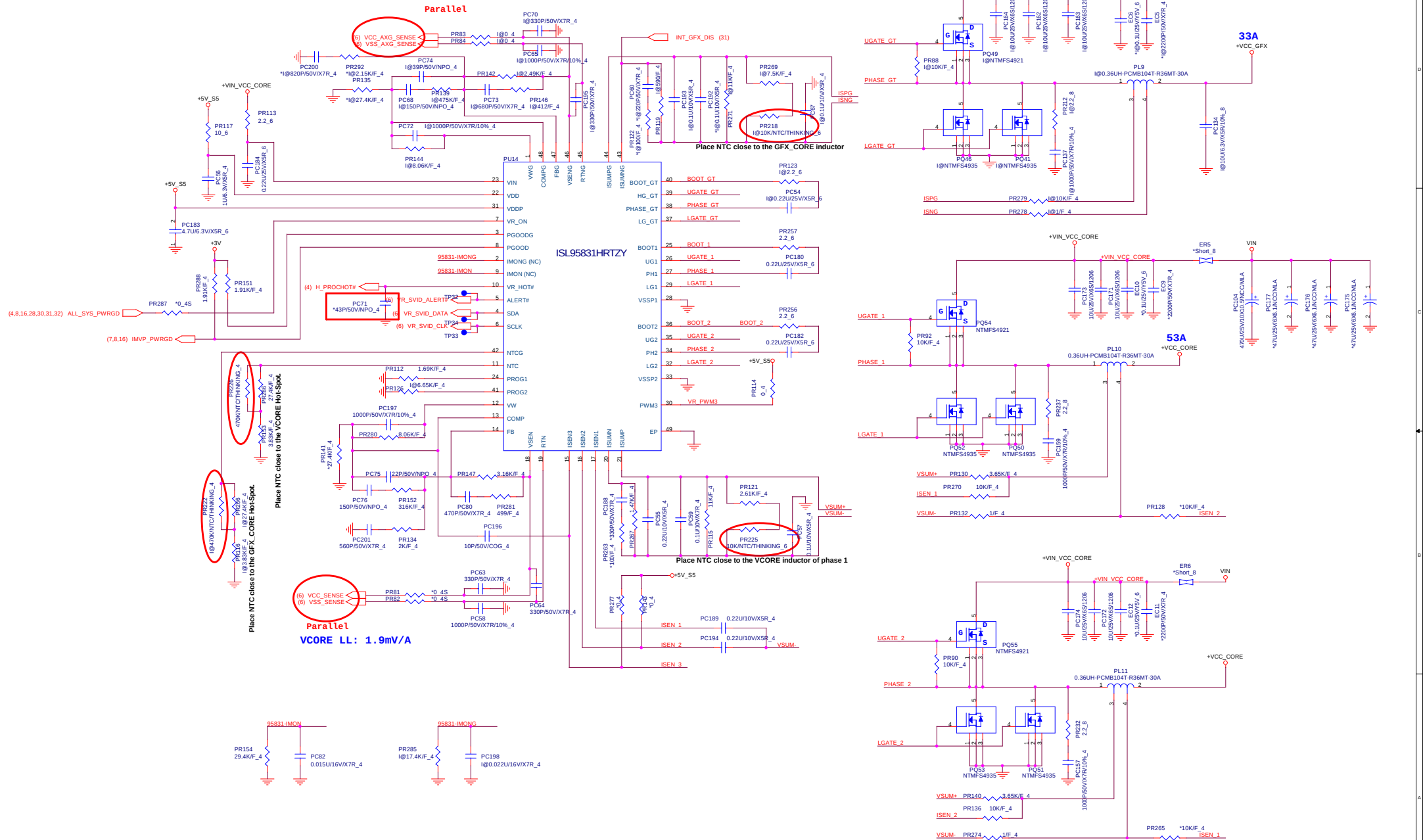


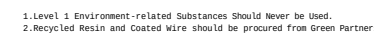
KEY BOARD Connector

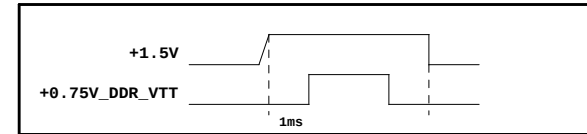
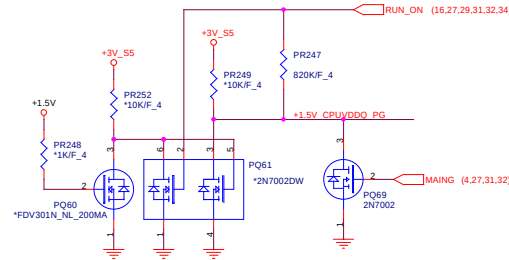
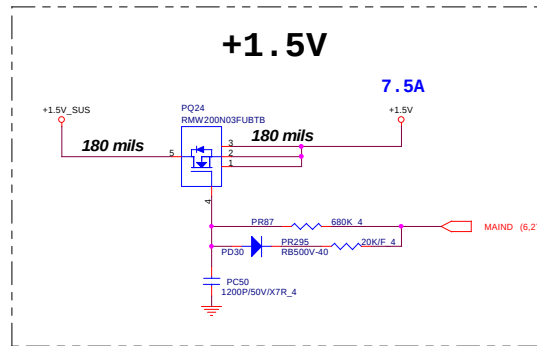
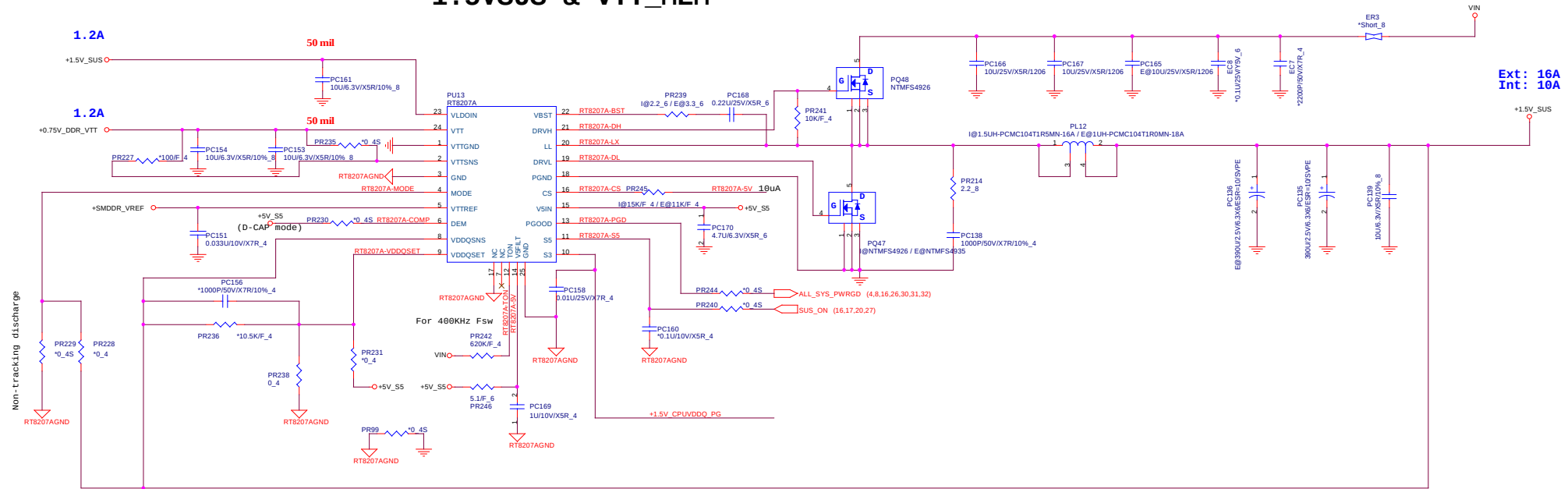


Power SW Board Connector







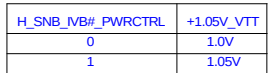


MODE	DISCHARGE MODE
+5V	No discharge
+1.5V	Tracking discharge
GND	Non-tracking discharge

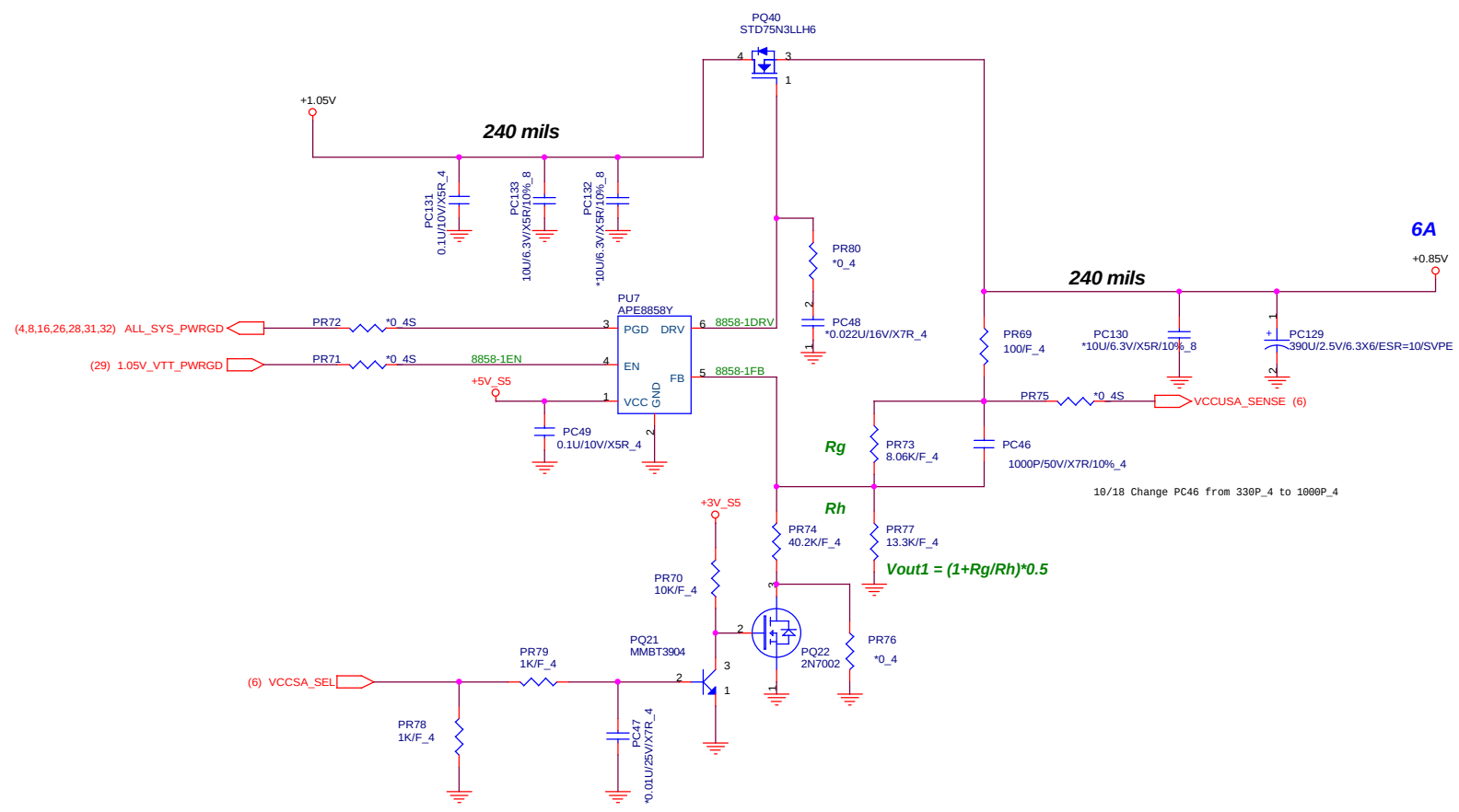
VDDQSET	VDDQ(V)	VTTREF & VTT	NOTE
GND	1.5 Fixed	VDDQSNS/2	DDR3
5V	1.8 Fixed	VDDQSNS/2	DDR2
FB-Resistor	Adjustable	VDDQSNS/2	1.5V<VDDQ<3V

VTT = VTTREF = VDDQSNS/2 = 0.75V

STATE	S3	S5	1.5VSUS	VTTREF	VTT
S0	1	1	on	on	on
S3	0	1	on	on	off
S4/S5	0	0	off	off	off



+0.85V



VID 0	VCCSA_SEL	+0.85V
0	0	0.9V
0	1	0.8V
1	0	0.75V
1	1	0.65V



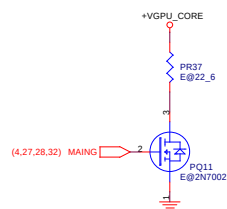
External VGA_CORE Voltage Setting

GF_X_CORE_CNTRL1	GF_X_CORE_CNTRL0	VGA_CORE	
0	0	1.025V	PR176=64.9K/F_4
0	1	1.00V	PR181=0_4
1	0	0.85V	PR180=16.9K/F_4
1	1	0.85V	PR39=3.24K/F_4

External VGA_CORE Voltage Setting

GFX_CORE_CNTRL1	GFX_CORE_CNTRL0	VGA_CORE	
0	0	1.025V	PR176=66.5K/F_4
0	1	1.00V	PR181=0_4
1	0	0.875V	PR180=15.4K/F_4
1	1	0.875V	PR39=3.09K/F_4

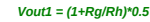
Dis-charge

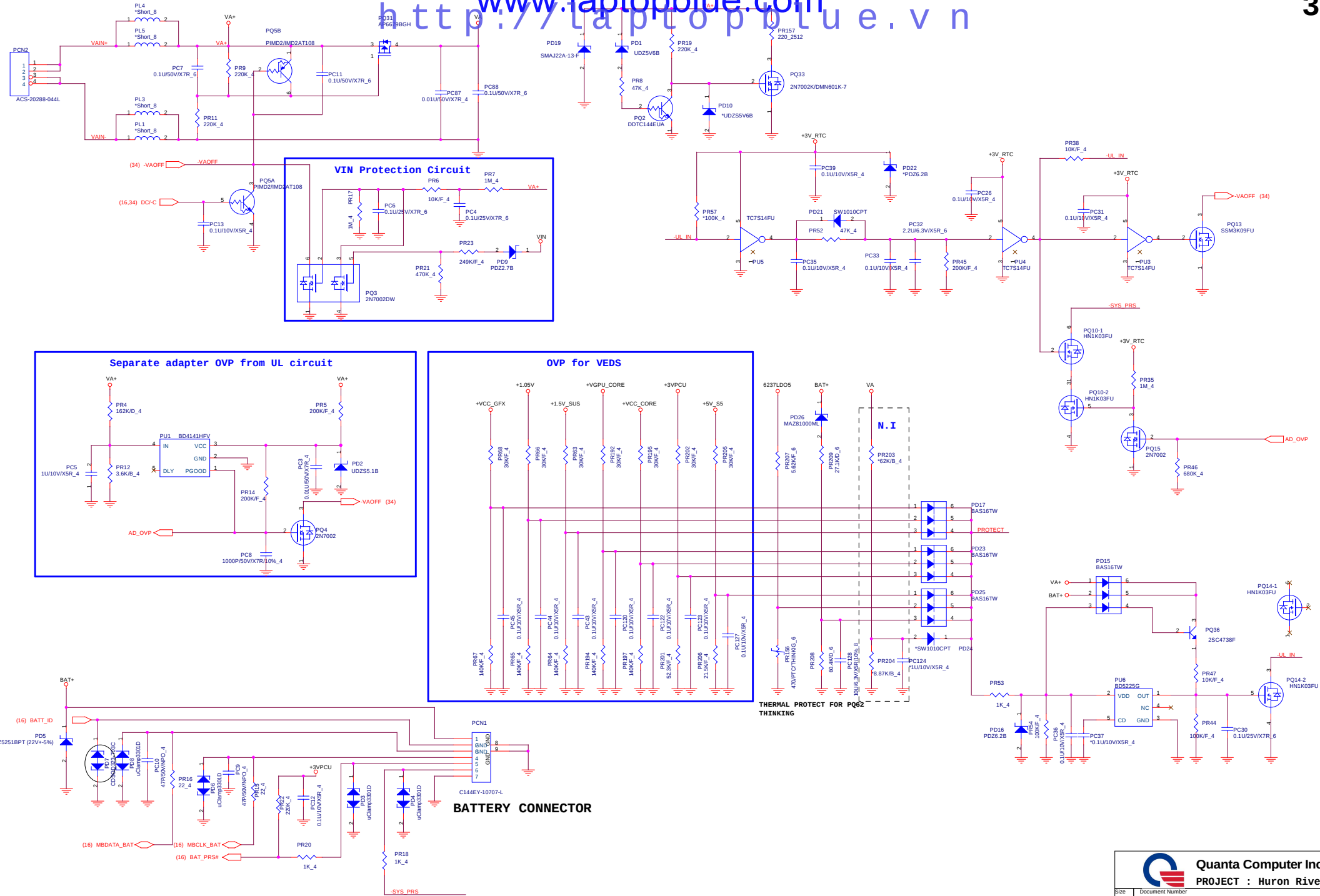


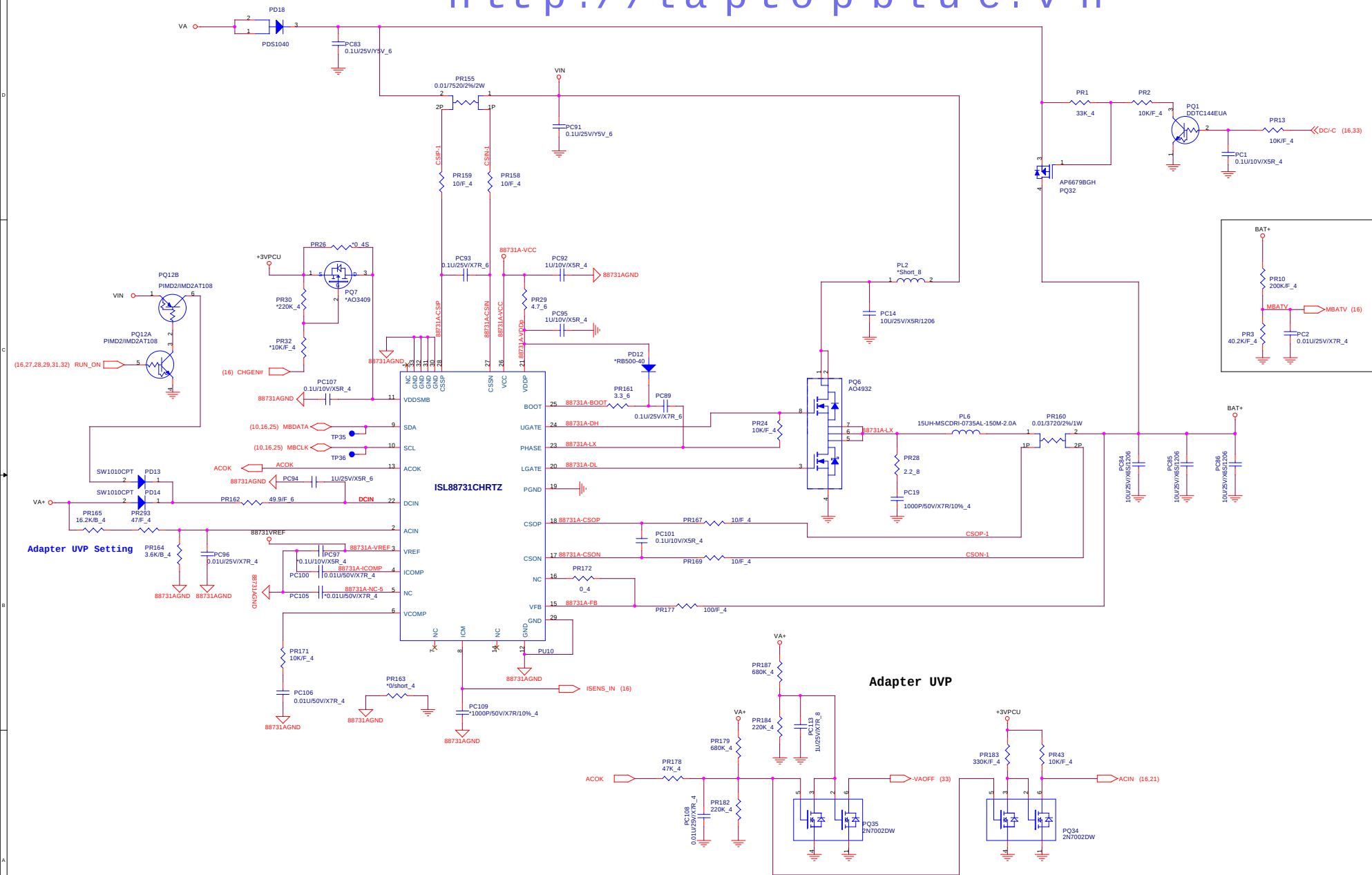
UMA GFX DISABLE



(0.1A) +3V
(2A) +1.05V
(6A) +1.5V









PROJECT : Huron River

1. Level 1 Environment-Related Substances should never be used.	Date: Tuesday, April 05, 2011			Sheet 35 of 39		
2. Recycled Resin and Coated Wire should be procured from Green Partners						

N12P-GV

External VGA_CORE Voltage Setting:

GFX_CORE_CNTRL1	GFX_CORE_CNTRL0	VGA_CORE	
0	0	1.025V	PR176=64.9K/F_4
0	1	1.00V	PR181=0_4
1	0	0.85V	PR180=16.9K/F_4
1	1	0.85V	PR39=3.24K/F_4

N12M-GS2

External VGA_CORE Voltage Setting:

GFX_CORE_CNTRL1	GFX_CORE_CNTRL0	VGA_CORE	
0	0	1.025V	PR176=66.5K/F_4
0	1	1.00V	PR181=0_4
1	0	0.875V	PR180=15.4K/F_4
1	1	0.875V	PR39=3.09K/F_4

Logical Strap Bit Mapping

Resistor Value	Pull to VDD	Pull to GND
5K	1000	0000
10K	1001	0001
15K	1010	0010
20K	1011	0011
25K	1100	0100
30K	1101	0101
35K	1110	0110
45K	1111	0111

N12M Strap Bit Define

Straps	Bit 3	Bit 2	Bit 1	Bit 0
ROM_S0	FB[1]	FB[1]	SMB_ALT_ADDR	VGA_DEVICE
ROM_SCLK	PCI_DEVID[4]	SUB_VENDOR	PCI_DEVID[5]	PEX_PLL_EN_TERM
ROM_SI	RAMCFG[3]	RAMCFG[2]	RAMCFG[1]	RAMCFG[0]
STRAP2	PCI_DEVID[3]	PCI_DEVID[2]	PCI_DEVID[1]	PCI_DEVID[0]
STRAP1	3GIO_PADCFG[3]	3GIO_PADCFG[2]	3GIO_PADCFG[1]	3GIO_PADCFG[0]
STRAP0	USER[3]	USER[2]	USER[1]	USER[0]
STRAP3	SOR3_EXPOSED	SOR2_EXPOSED	SOR1_EXPOSED	SOR0_EXPOSED
STRAP4	RESERVED	RESERVED	PCIE_MAX_SPEED	DP_PLL_VDD33

for device ID

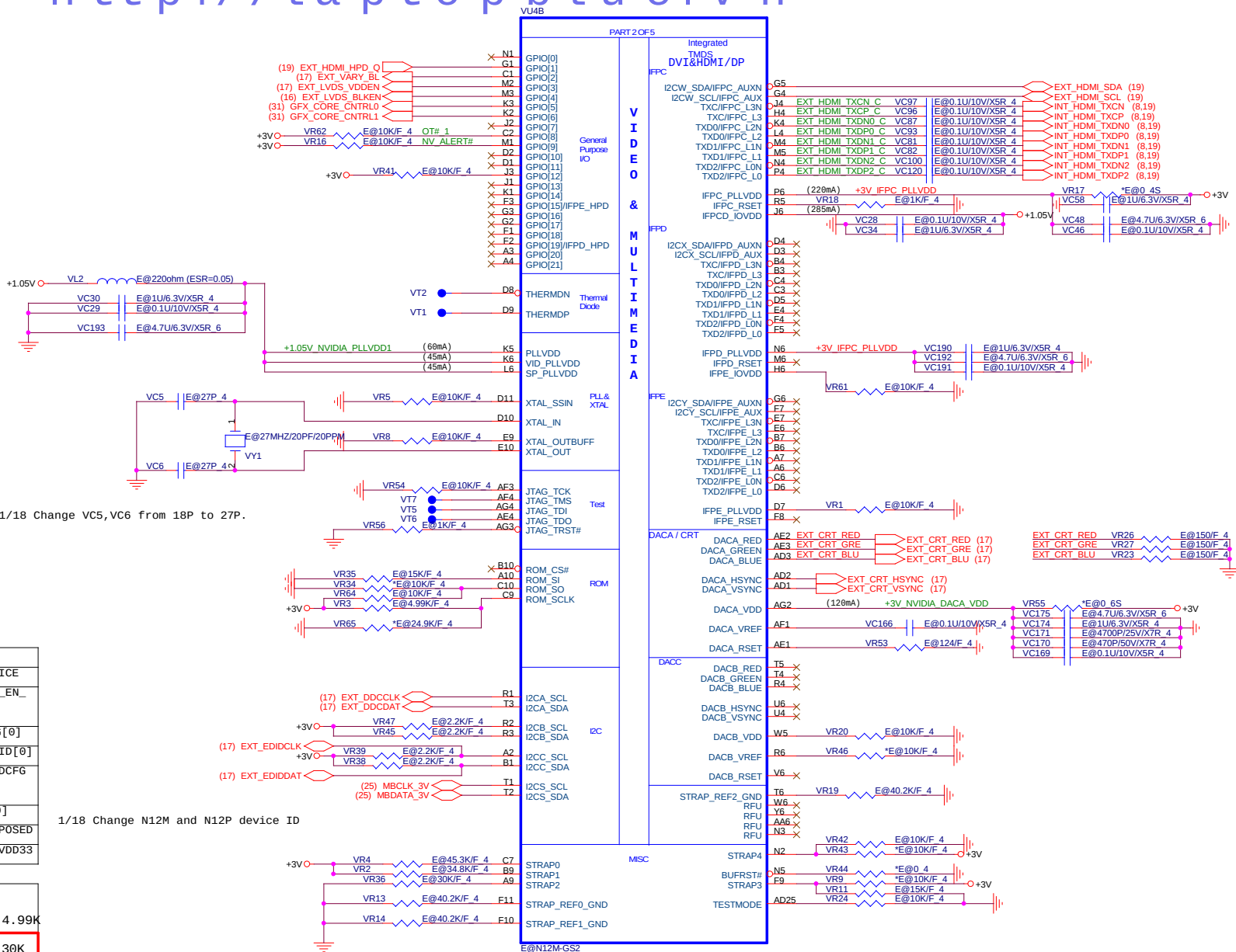
For N12P VR3 pull high 4.99K VR65 NC VR36 Pull down 4.99K

For N12M VR3 pull high 4.99K VR65 NC VR36 Pull down 30K

	VRAM Capacity	VRAM Vender	ID	VR35	P/N	P/N
N12M	64Mx16 DDR3	Hynix	0010	PD15K	H5TQ1G63DFR-12C	
		Samsung	0011	PD20K	K4W1G1646G-BC12	
	128Mx16 DDR3	Hynix	0110	PD35K	H5TQ2G63BFR-12C	
		Samsung	0111	PD45K	K4W2G1646C-HC12	

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36



Down Side VRAM TOP/BOT

TOP

BOT

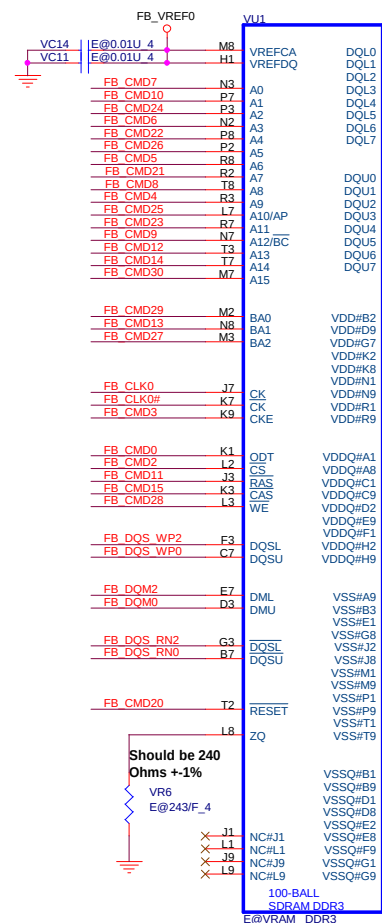
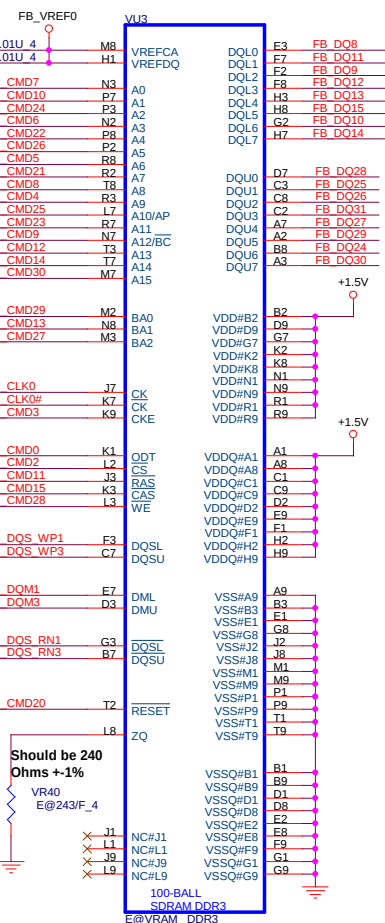
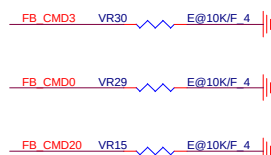
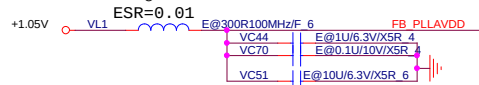
VU4C Part 3 of 5

MEMORY INTERFACE

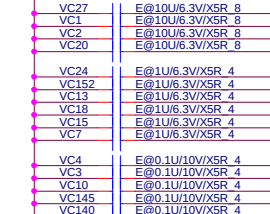
write strobe read strobe

E@N12M-GS2

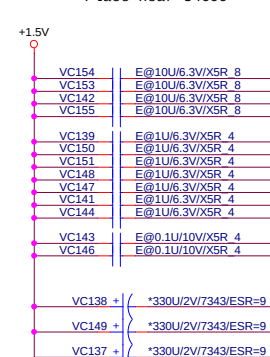
need find Bead 30
ohm@100Mhz
ESR=0.01



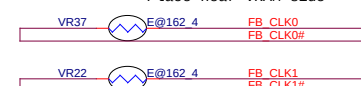
Place near U4005



Place near U4006



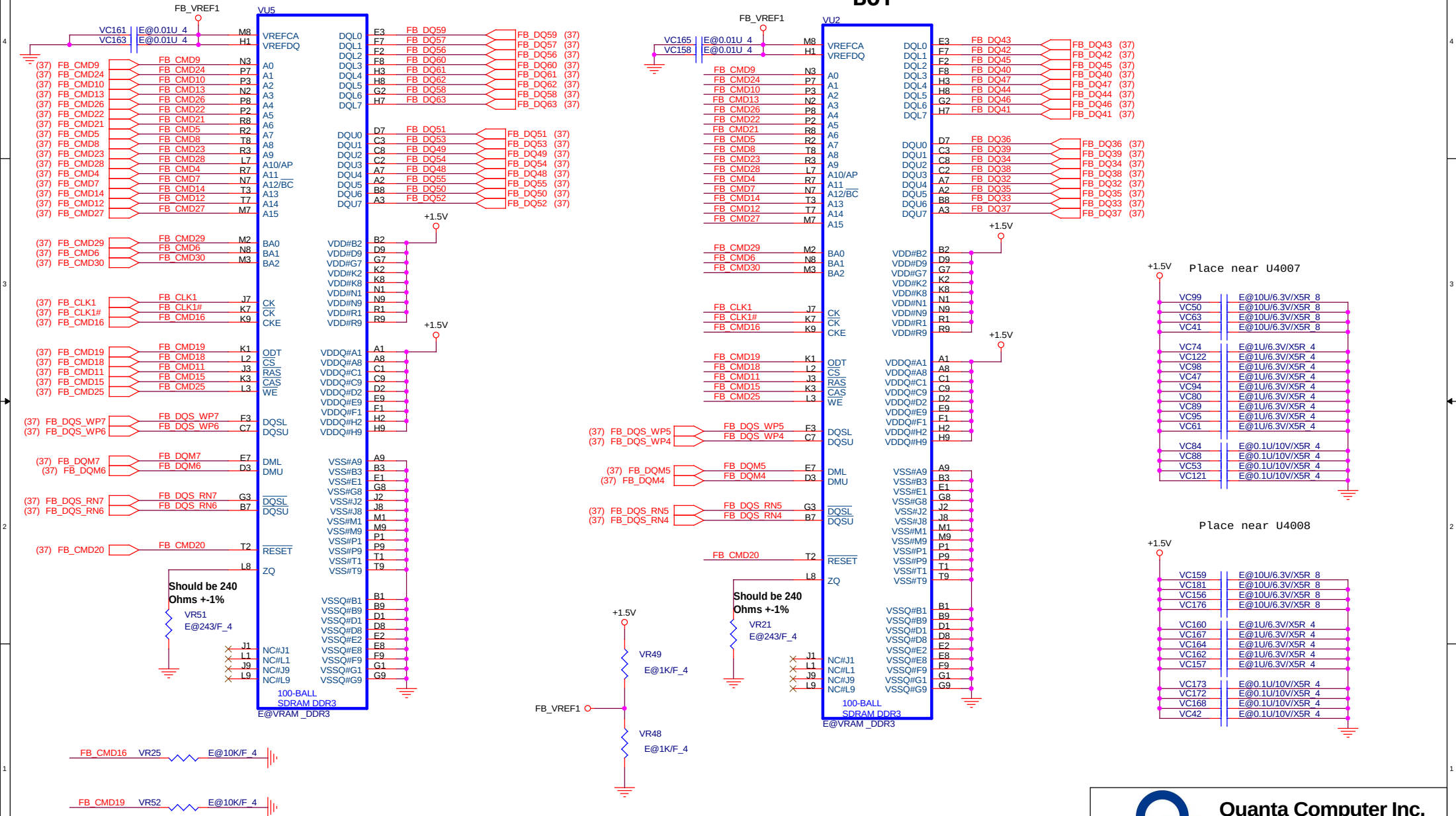
Place near VRAM side



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PROJECT : Huron River
VRAM 3/4

TOP

BOT



USB PORT Architecture for EVT	
PORT 0	IO Port
PORT 1	IO Port
PORT 2	IO Port
PORT 3	N/A
PORT 4	IO Port
PORT 5	N/A
PORT 6	N/A
PORT 7	N/A
PORT 8	N/A
PORT 9	Camera
PORT 10	N/A
PORT 11	N/A
PORT 12	WiMax
PORT 13	BlueTooth

PCIE BUS	
PORT 1	WLAN Port
PORT 2	N/A
PORT 3	N/A
PORT 4	CARD READER
PORT 5	N/A
PORT 6	GLAN(RTL8111E)
PORT 7	N/A
PORT 8	N/A

SM BUS	MBCLK/MBDATA
ISL88731CHRTZ	0001 001X
NVIDIA	1001 111X

SATA BUS	
PORT 0	HDD
PORT 1	N/A
PORT 2	N/A
PORT 3	ODD
PORT 4	N/A

Board ID0 (N12M/N12P)	N12M	N12P
R294	Stuff	No Stuff
R297	No Stuff	Stuff

Board ID1 (VRAM Vendor)	Samaung	Hynix
R47	Stuff	No Stuff
R48	No Stuff	Stuff

Board ID2 (VRAM 1G/512M)	1G	512M
R39	Stuff	No Stuff
R27	No Stuff	Stuff