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WCDMA B1

WCDMA B5

WCDMA B8

GSM850/900RX

close to MT6166

800/PCS1900RX/B34/B39

Route AUXADC_GND with 24mil trace width under AUXADC_REF/AUXADC_TSX trace

Connect TSX/XTAL GND to GND_AUXADC first than connect to main GND

Route AUXADC_REF/AUXADC_TSX as differential trace with well GND shielding and route AUXADC_GND with 24mil trace width under AUXADC_TSX/AUXADC_REF trace to provide return current path.

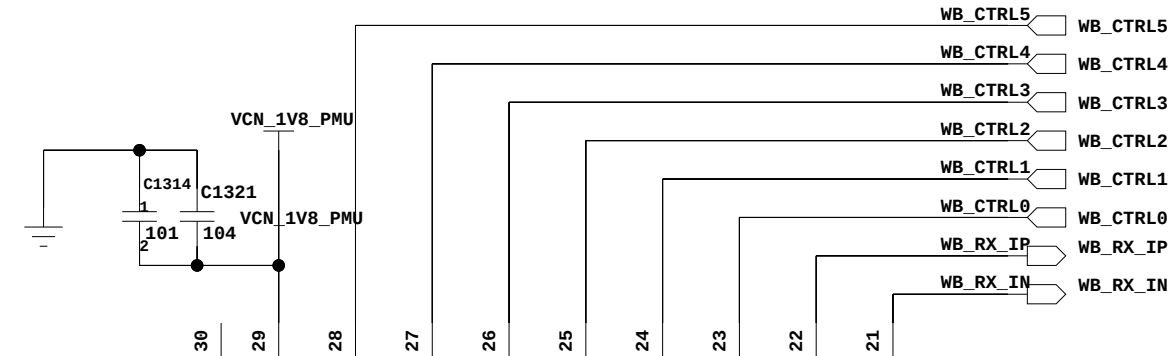
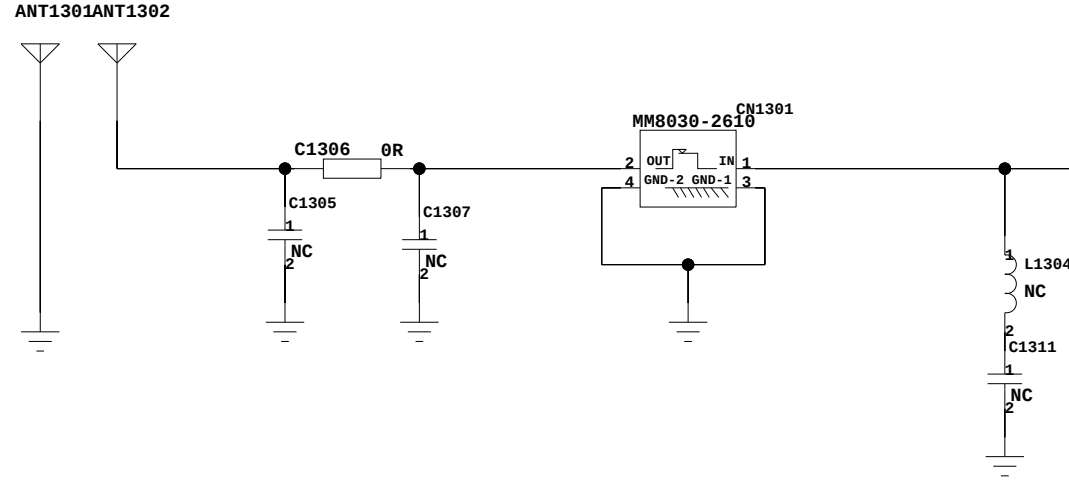
Two Application Circuit Conditions,

- 1.TSX Circuit : X600=TSX, R653=R656=NC, R654=100K+-1%, R655=R657=0ohm
- 2.XTAL Circuit :X600=Mobile XTAL, R653=R656=0ohm, R654=R655=R657=NC

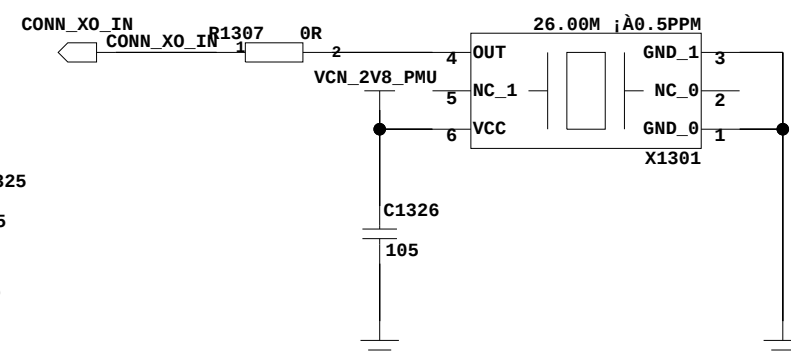
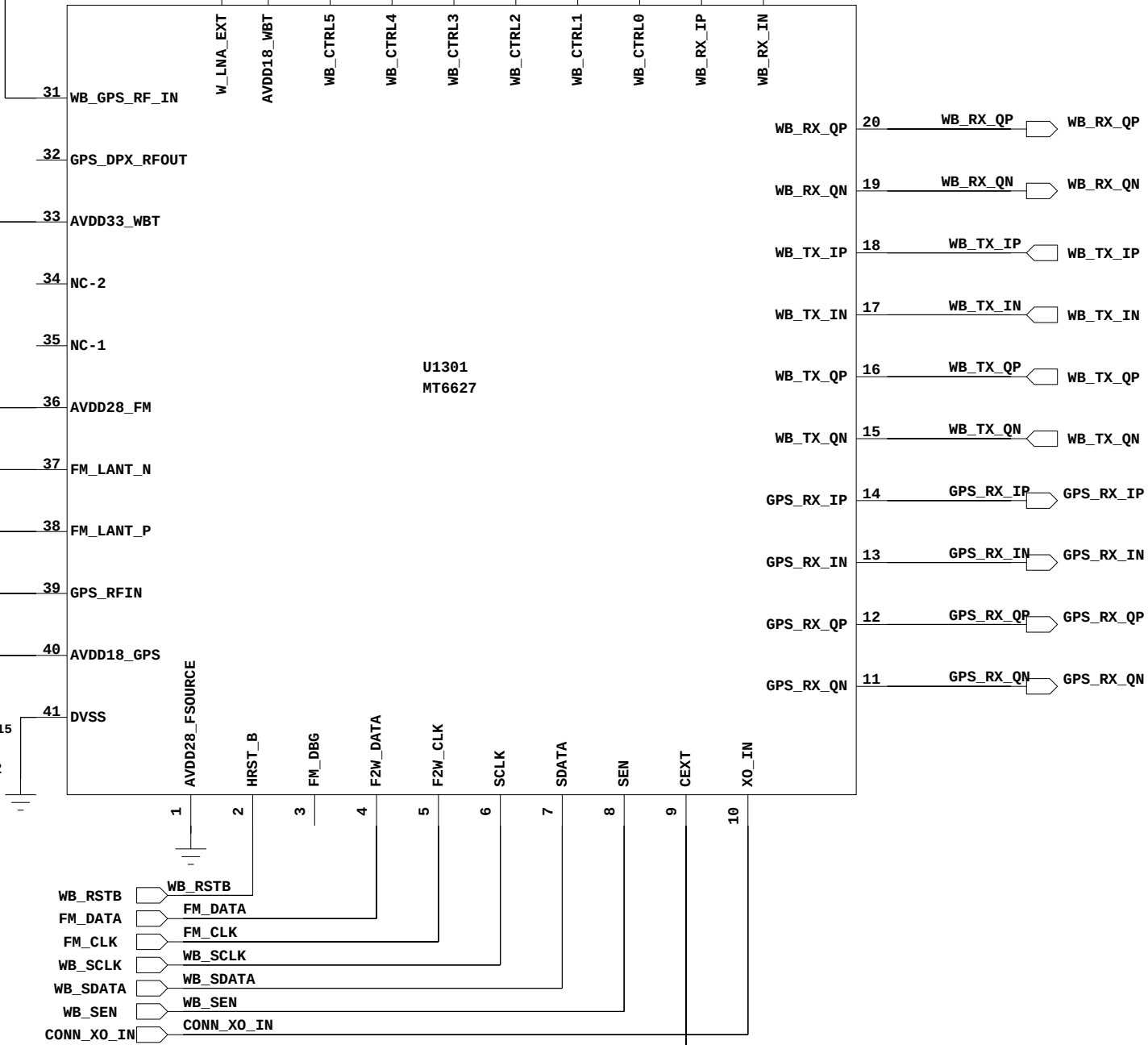
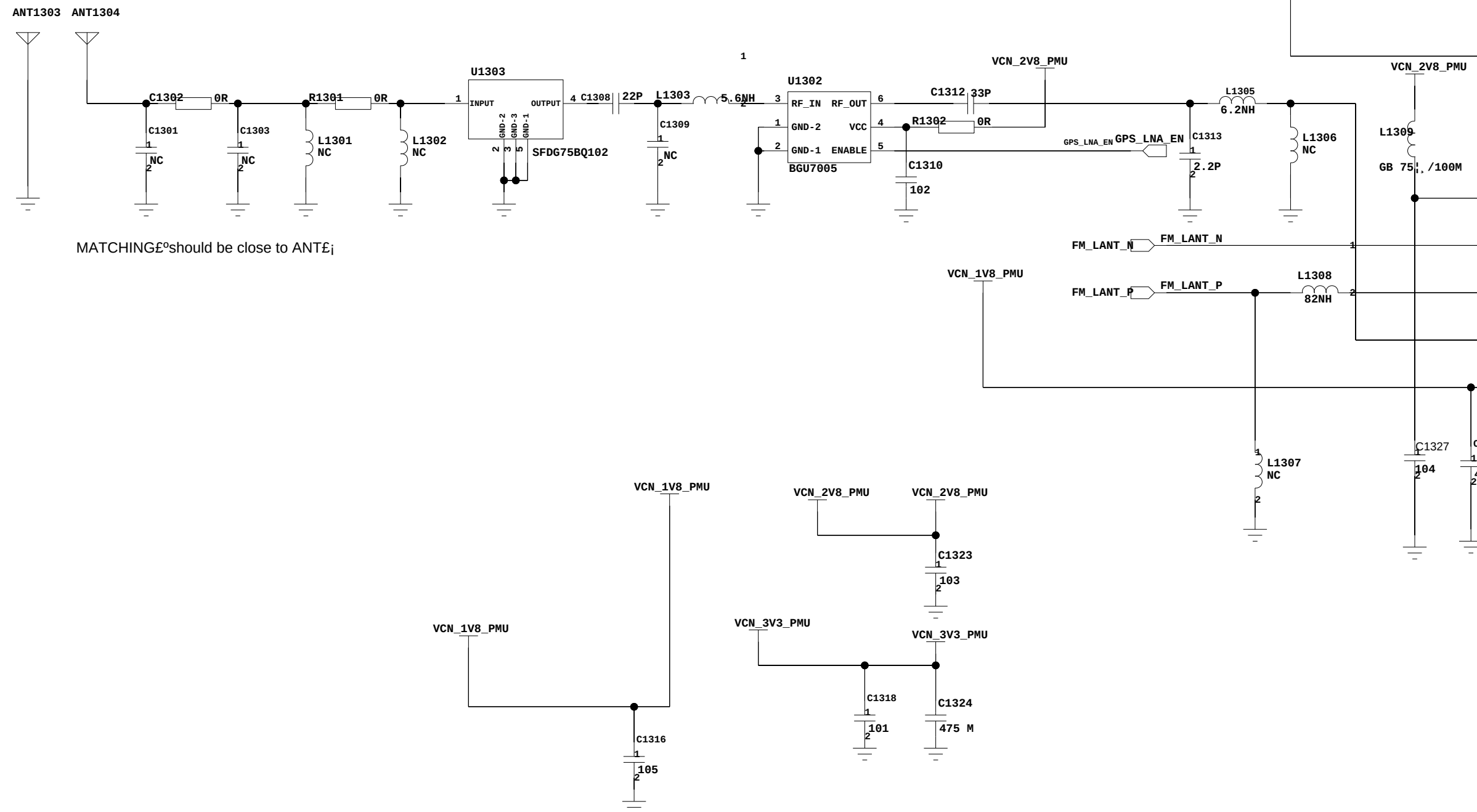
MODE	Logic	DCXO 32K_EN	XMODE	VXODIG
VTCXO		0(GND)	0(GND)	1(VIO18)
DCXO + 32K XO		0(GND)	1(VIO18)	1(VIO18)
DCXO + 32K-Less		1(VTXCO28)	1(VTXCO28)	1(VTXCO28)

	Sign	Date	Type:
Drawn By			Draw No: A128488TSA
Checked By			Sheet 12 of 12 M#6166 Ver:
Standard By			oppo
Approved By			

WIFI/Bluetooth



GPS



	Sign	Date	Type:	
Drawn By			Draw No: 213059-SA	
Checked By			Sheet 12 of	Ver: 0.0
Standard By				
Approved By				

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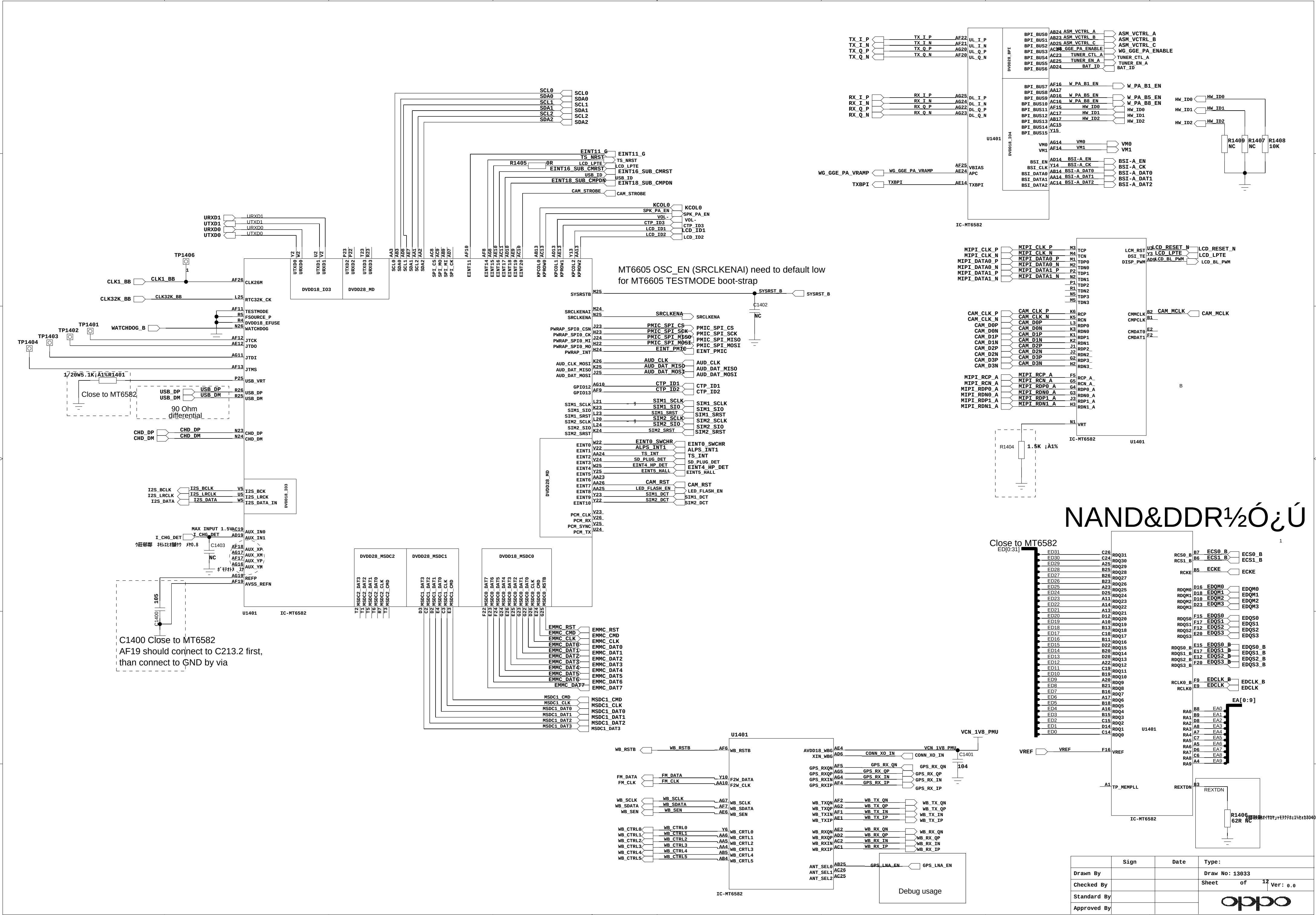
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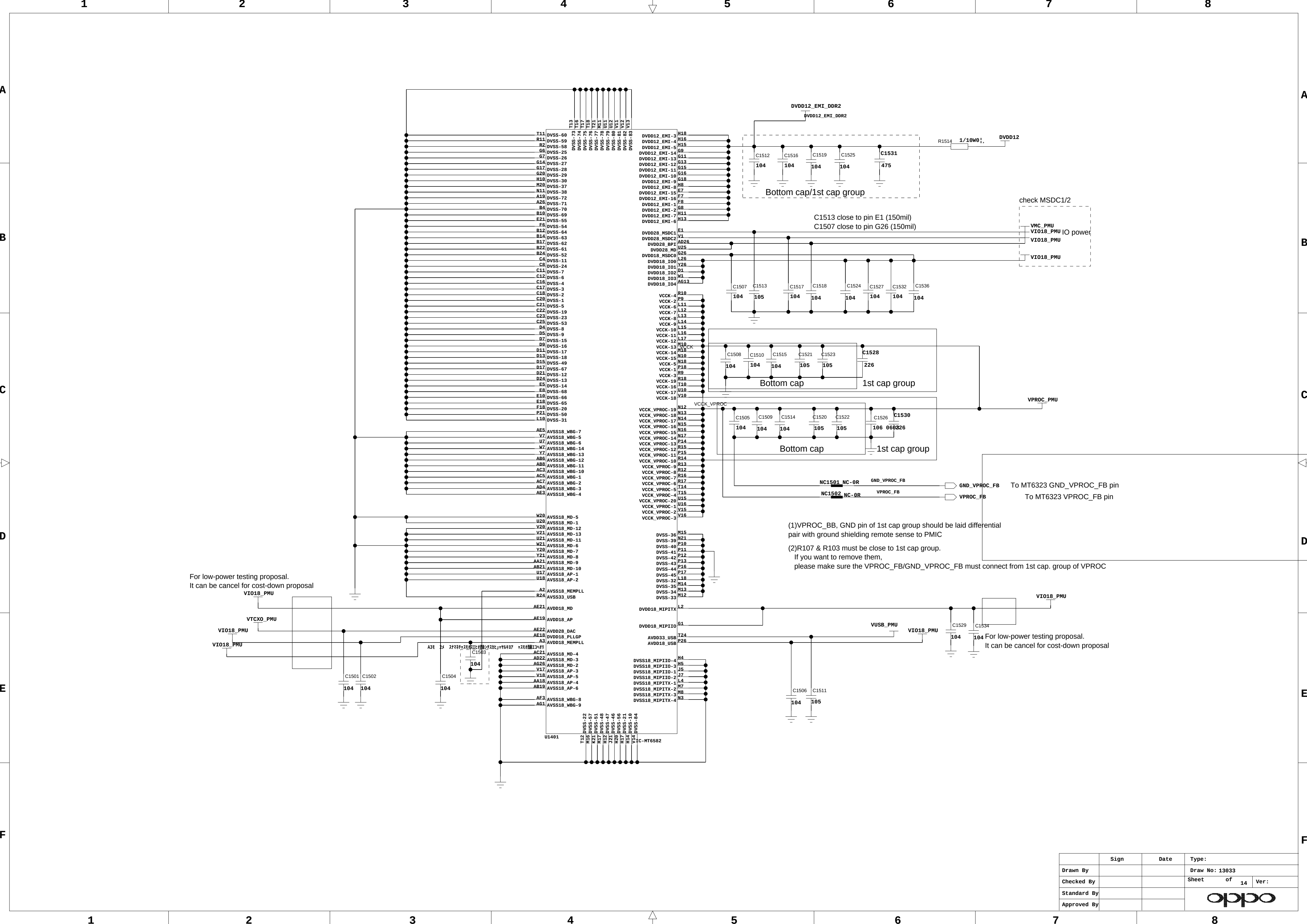
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NAND&DDR1/2Ó¿Ú

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Drawn By			Draw No: 13033
Checked By			Sheet of 12 Ver: 0.0
Standard By			
Approved By			





For low-power testing proposal.
It can be cancel for cost-down proposal

To MT6323 GND_VPROC_FB pin
To MT6323 VPROC_FB pin

- (1)VPROC_BB, GND pin of 1st cap group should be laid differential pair with ground shielding remote sense to PMIC
- (2)R107 & R103 must be close to 1st cap group.
If you want to remove them,
please make sure the VPROC_FB/GND_VPROC_FB must connect from 1st cap. group of VPROC

For low-power testing proposal.
It can be cancel for cost-down proposal

	Sign	Date	Type:
Drawn By			Draw No: 13933
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Standard By			oppo
Approved By			

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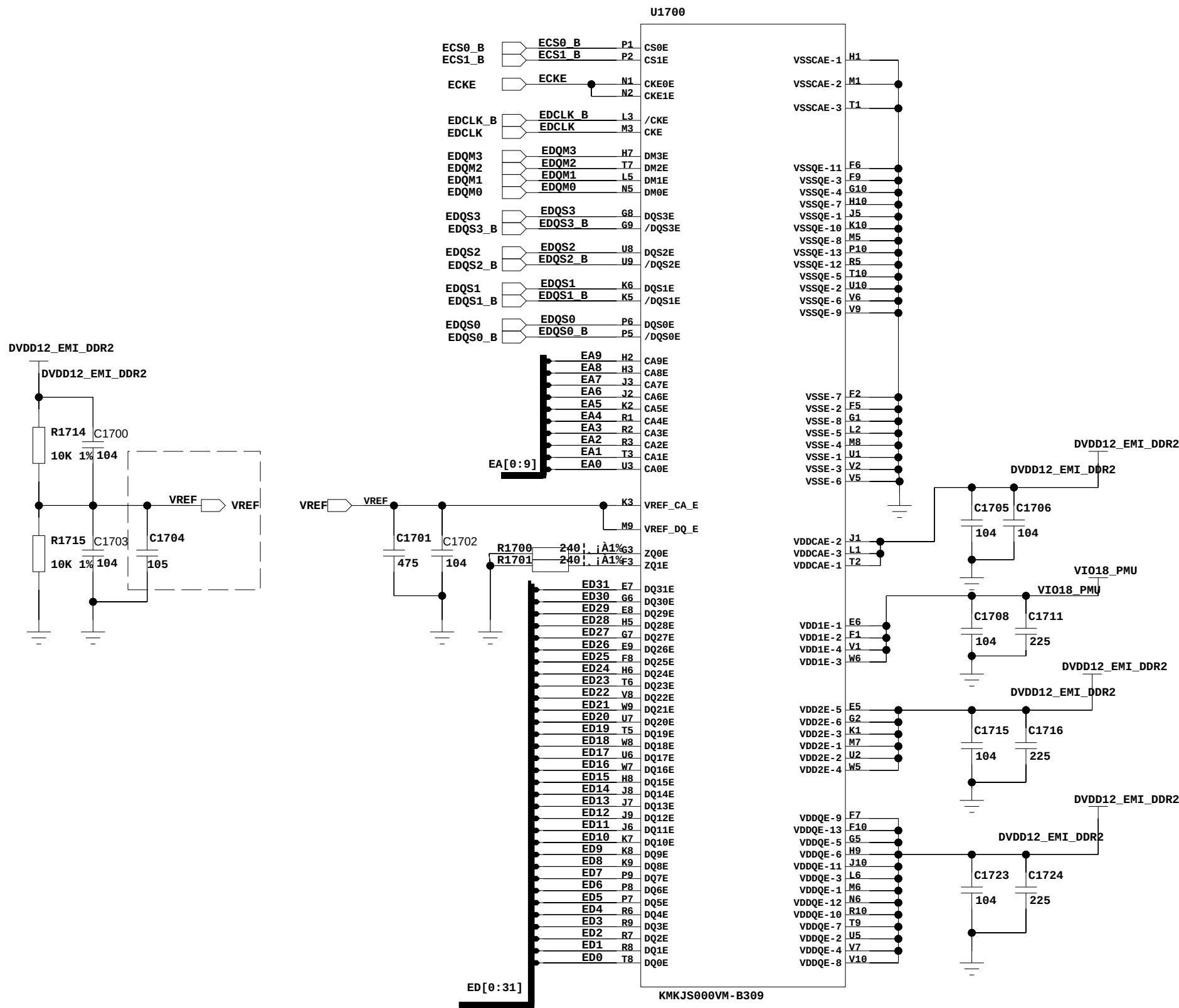
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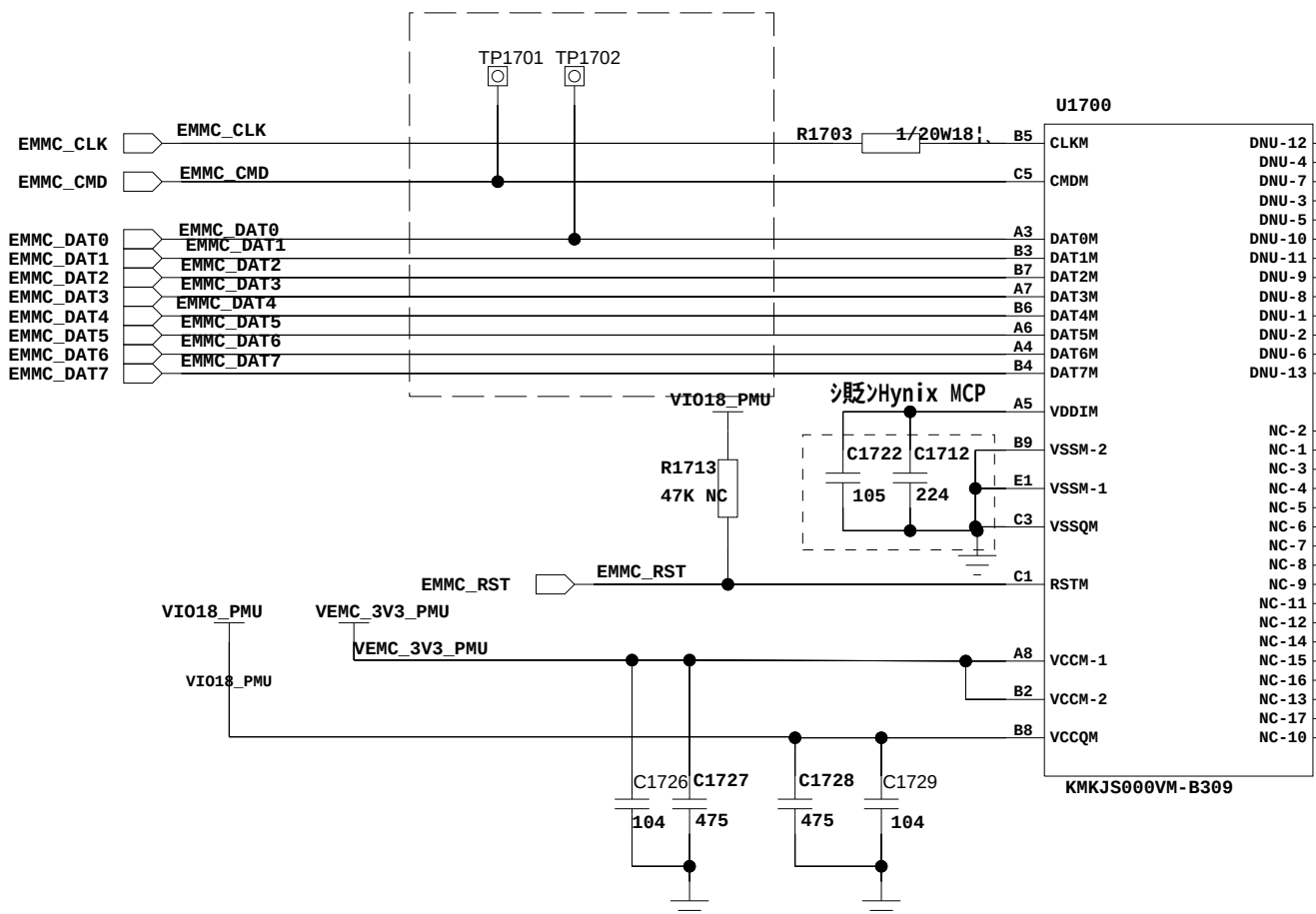
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DDR2

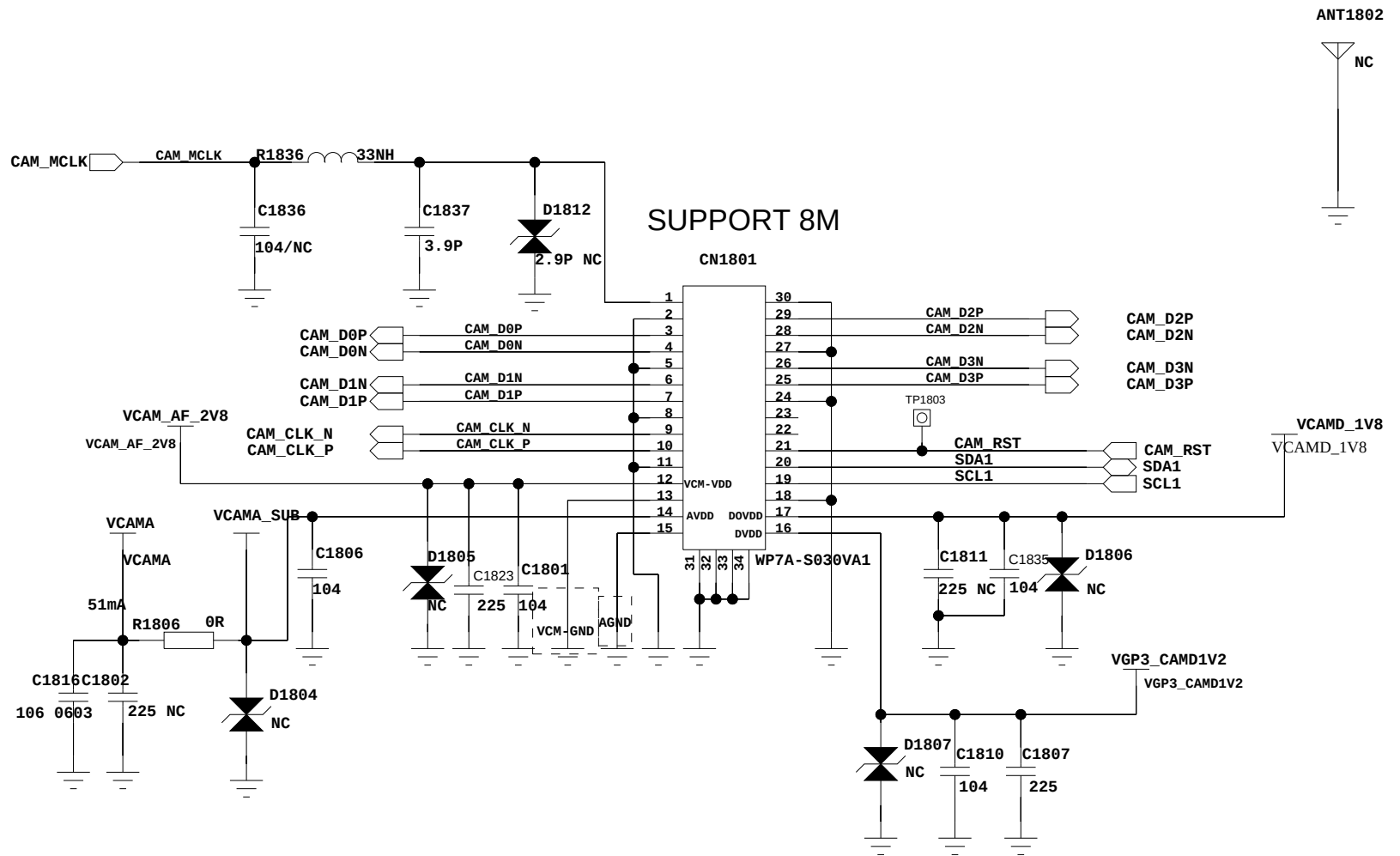


EMMC

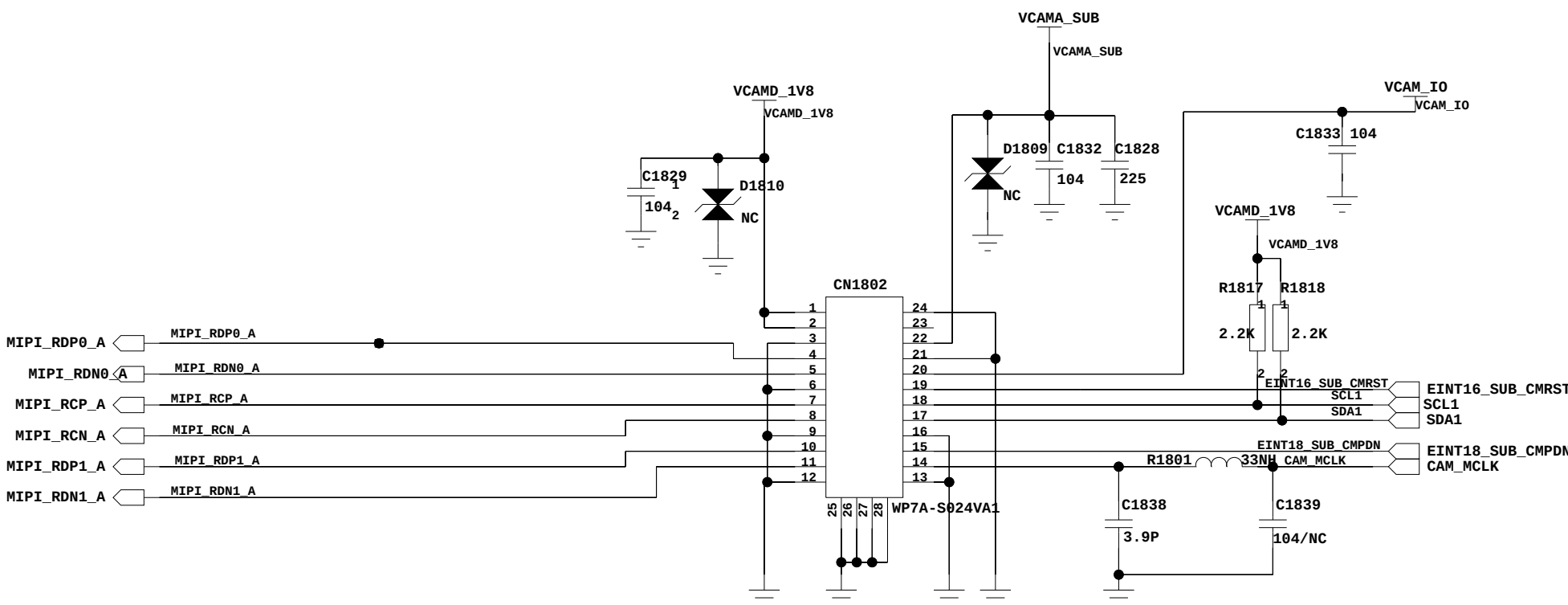


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Checked By			Sheet of 12 Ver: 0.0
Standard By			oppo
Approved By			

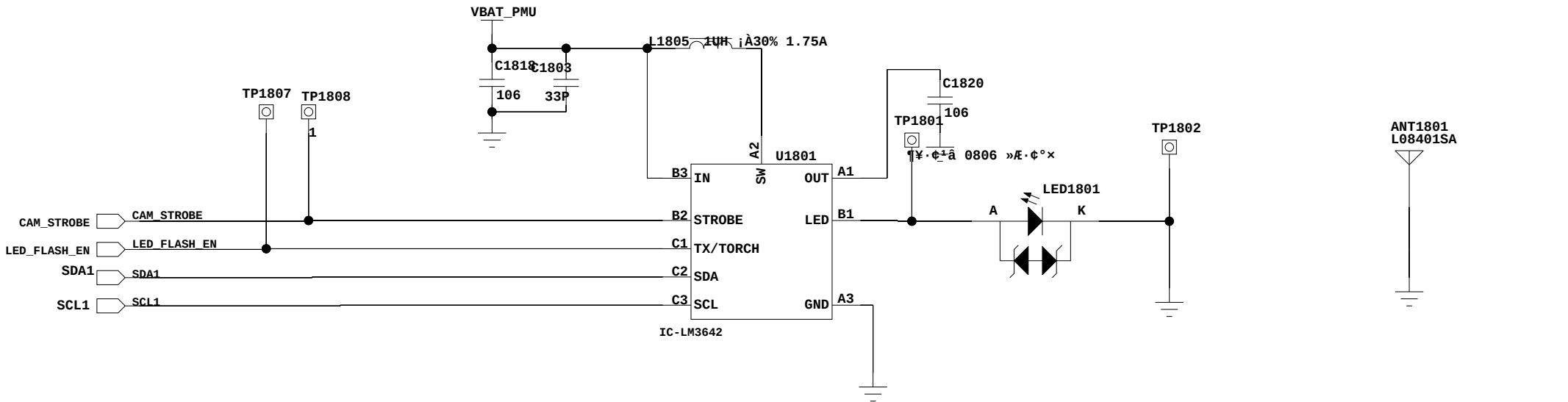
CAMERA



SUB_CAM

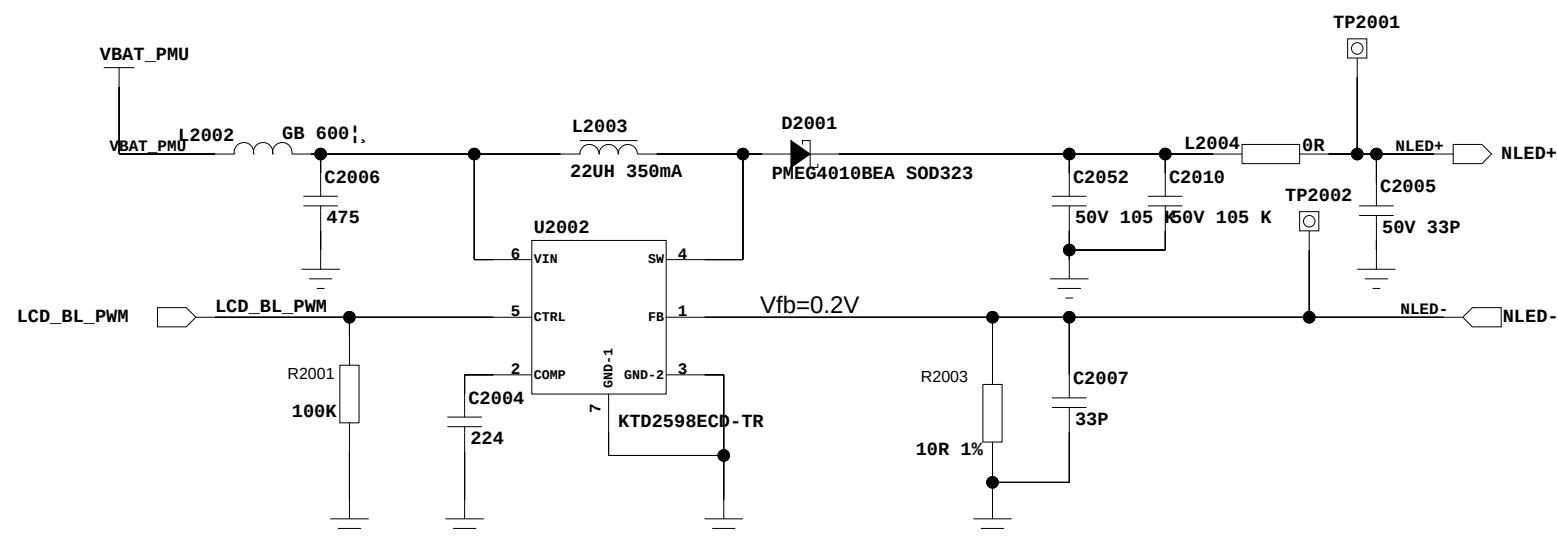


Flash LED Driver

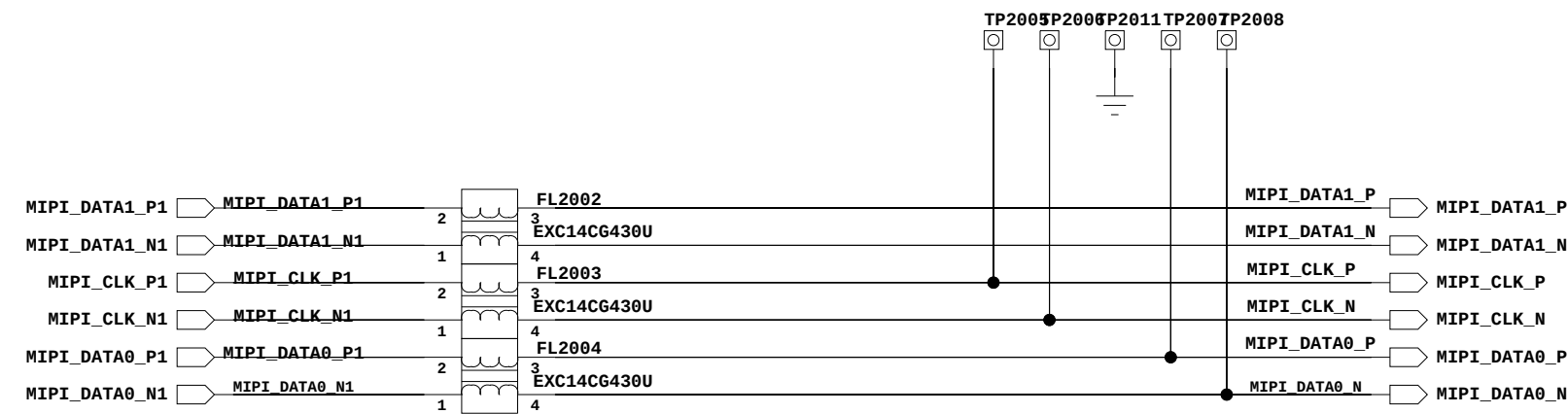
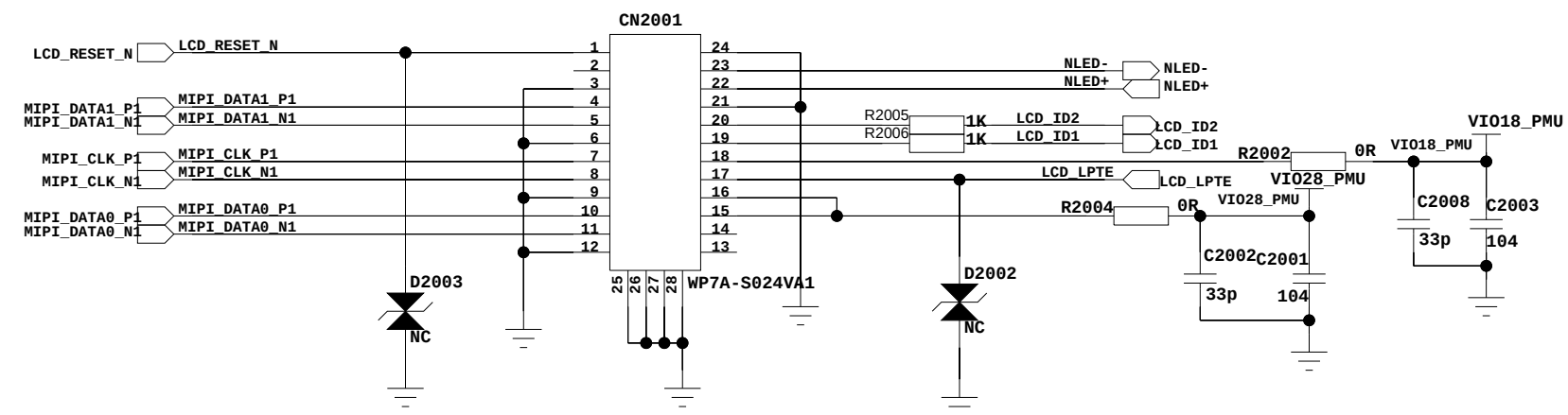


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Checked By			Sheet 18_Audio65IN+VFKEY
Standard By			oppo
Approved By			

LCD BACKLIGHT



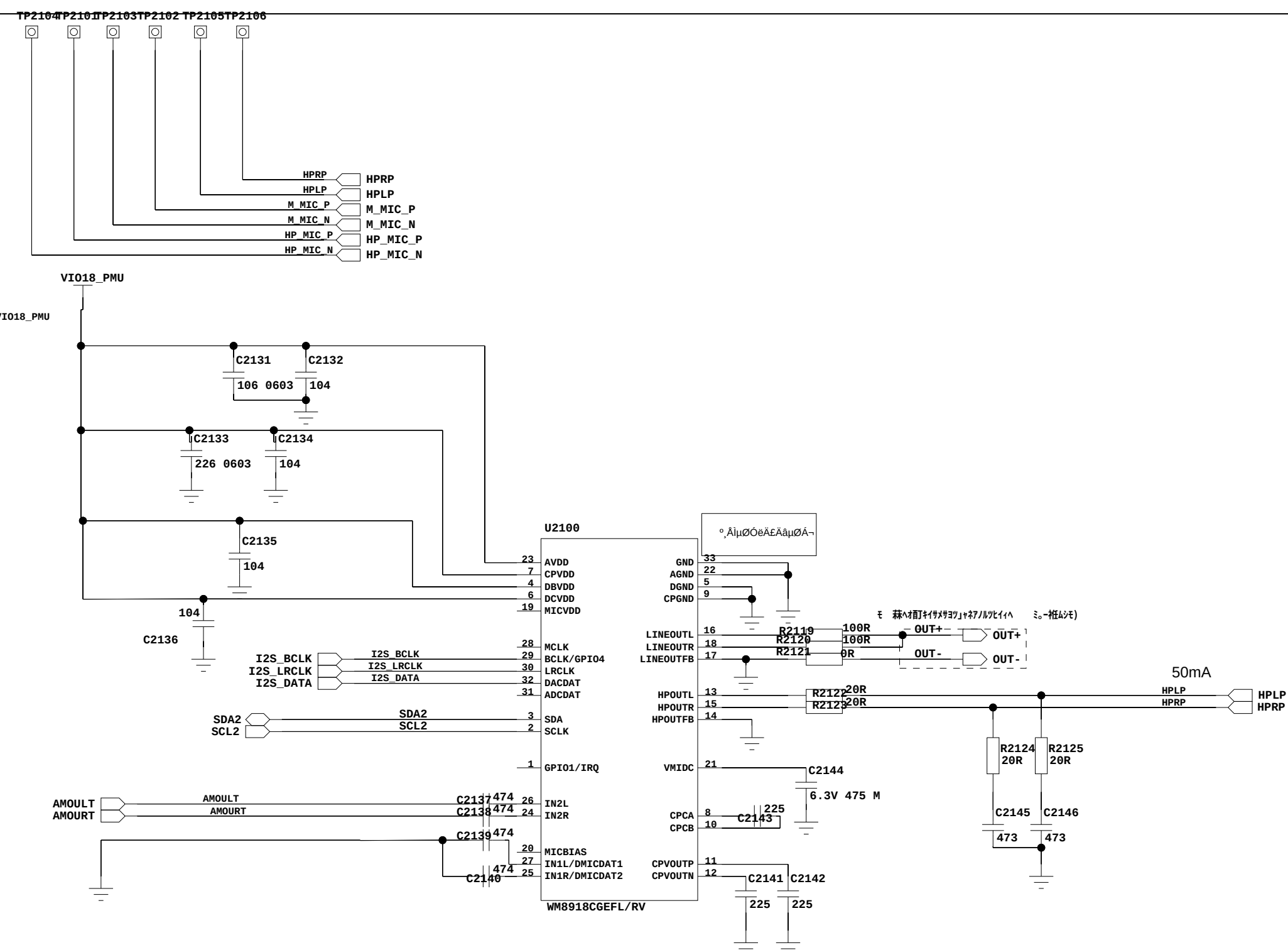
LCD MODULE



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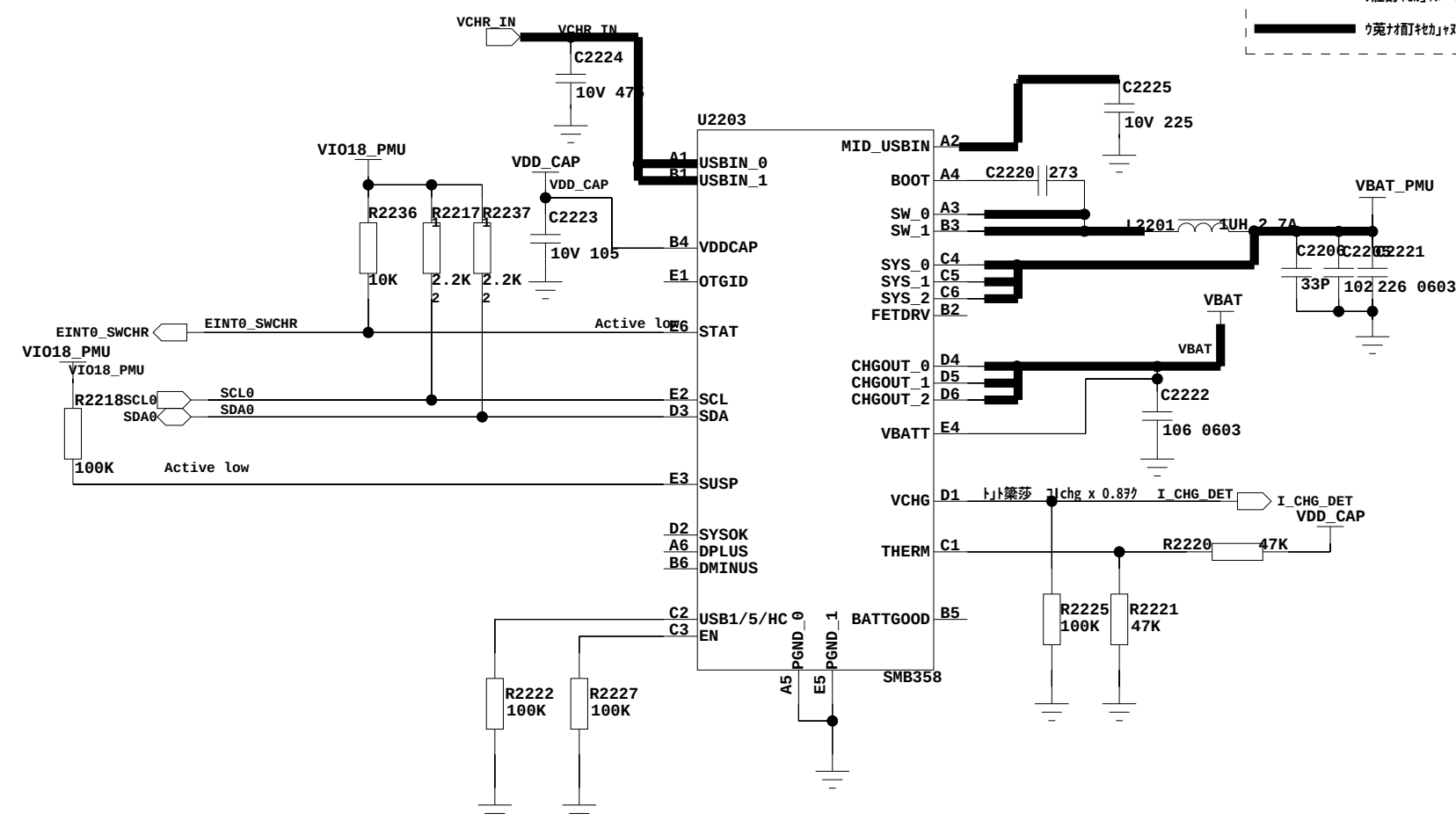


ACCDDET have 3 steps for headphone detect and hookint



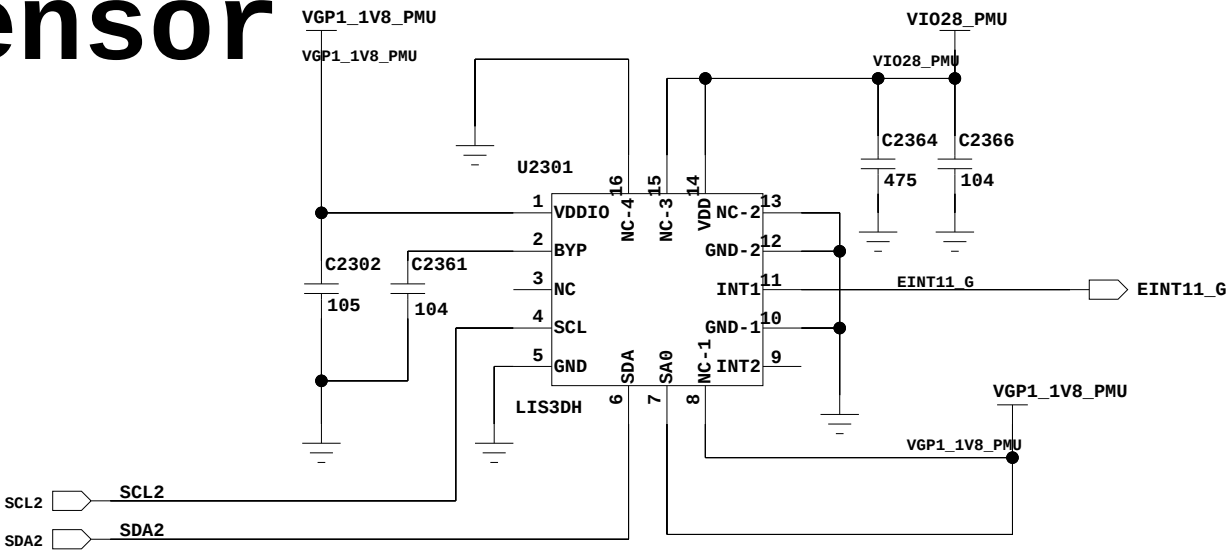
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Drawn By			Draw No: 13033	
Checked By			Sheet	21 of 100 Ver:
Standard By				
Approved By				

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 ■■■■■ ウ英ナ才訂キセカ」ヤヌ I2A才鄰 °マ°

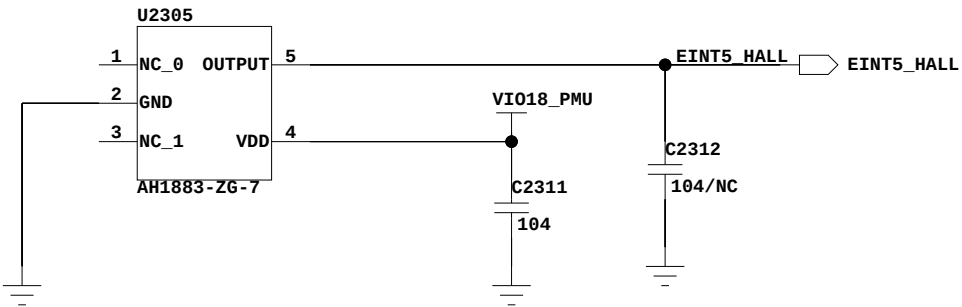
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Standard By				
Approved By				

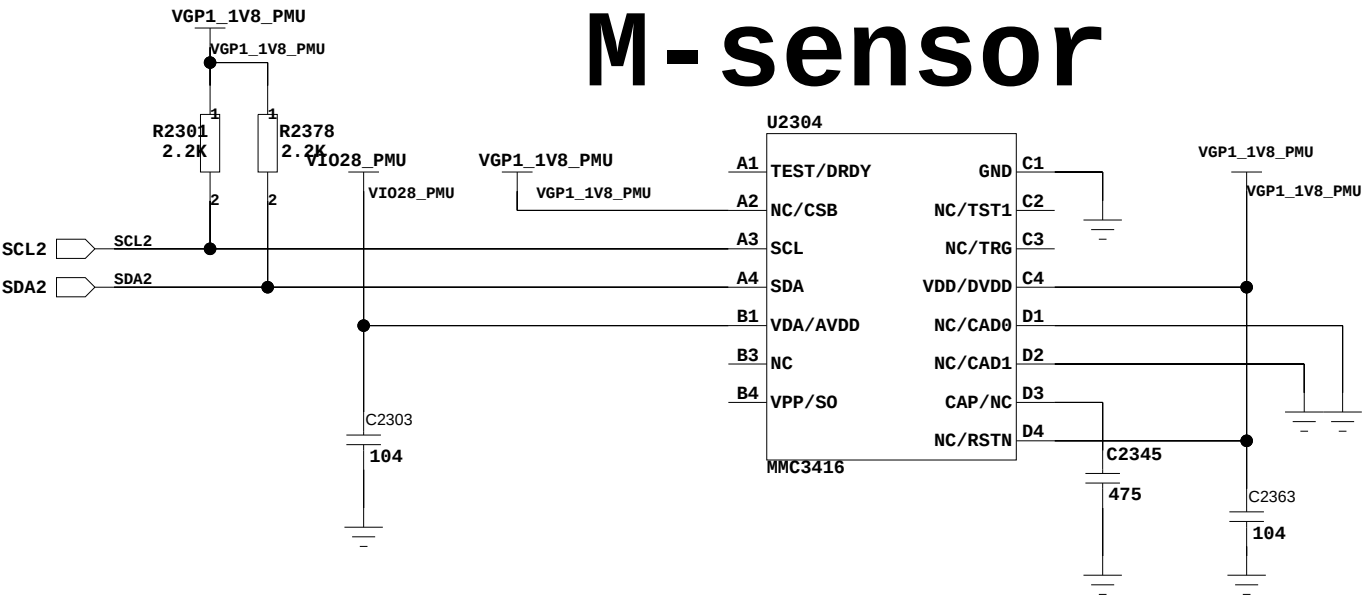
G - sensor



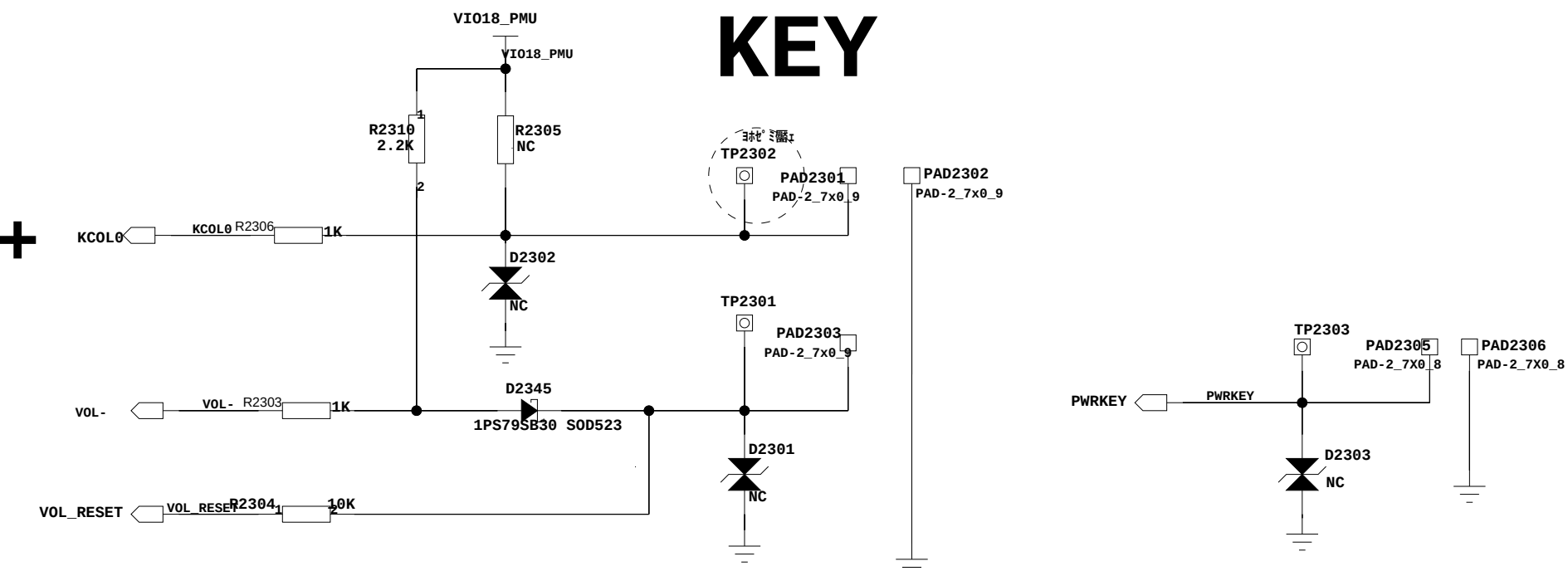
Hall-sensor



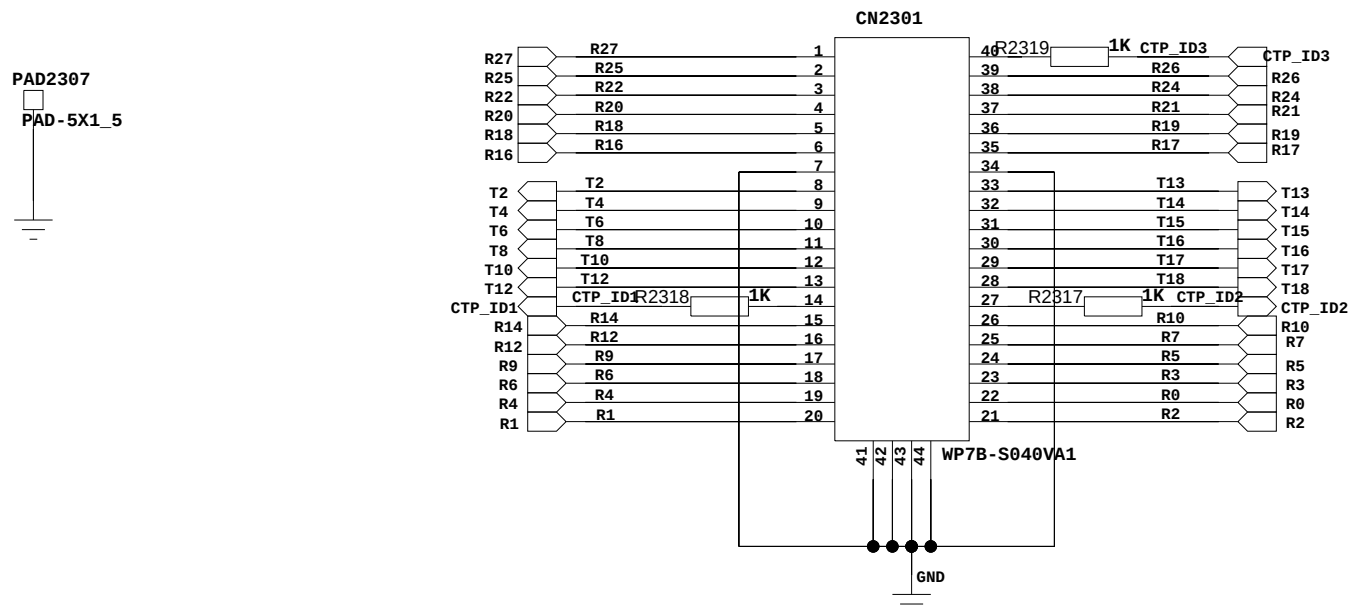
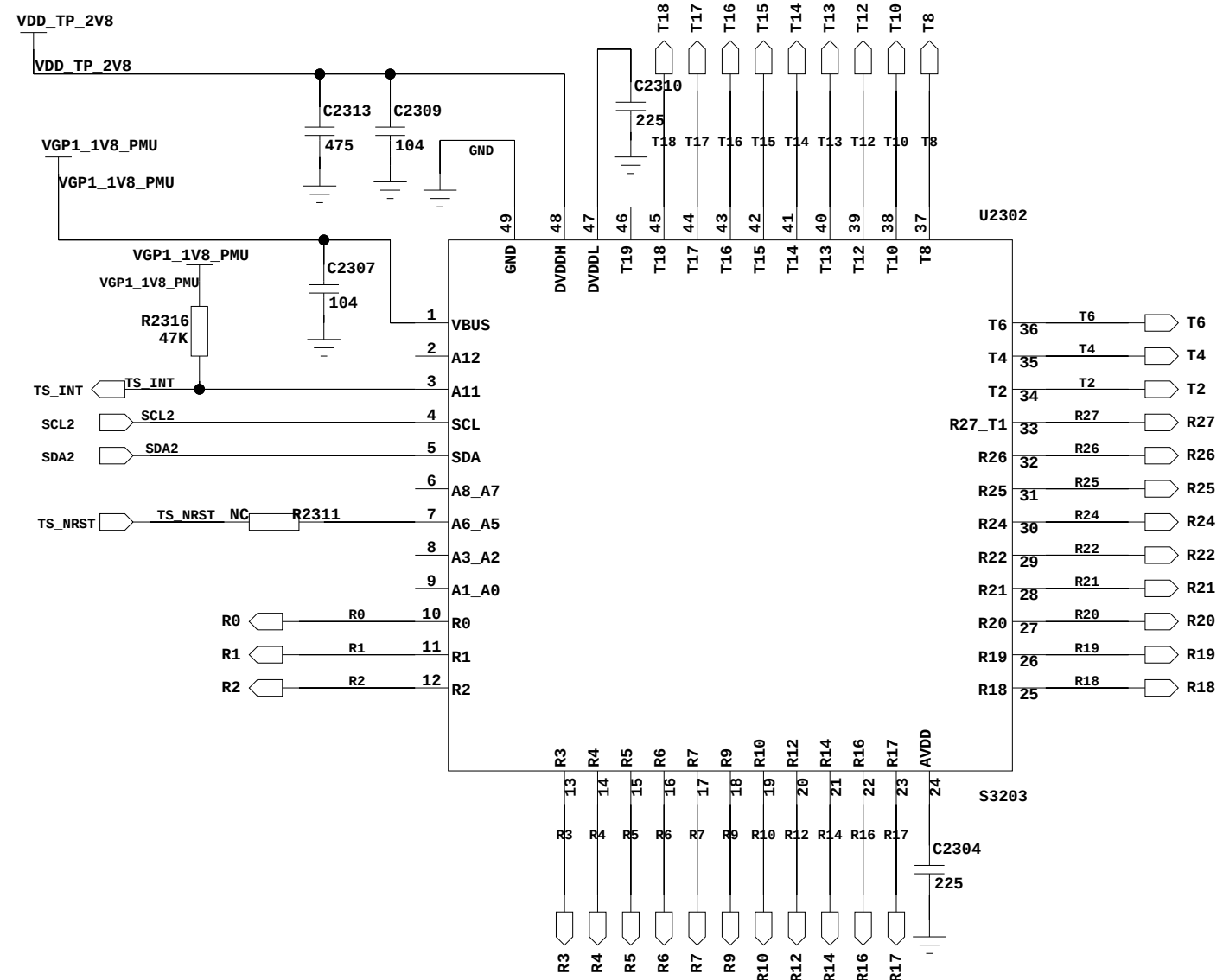
M - sensor



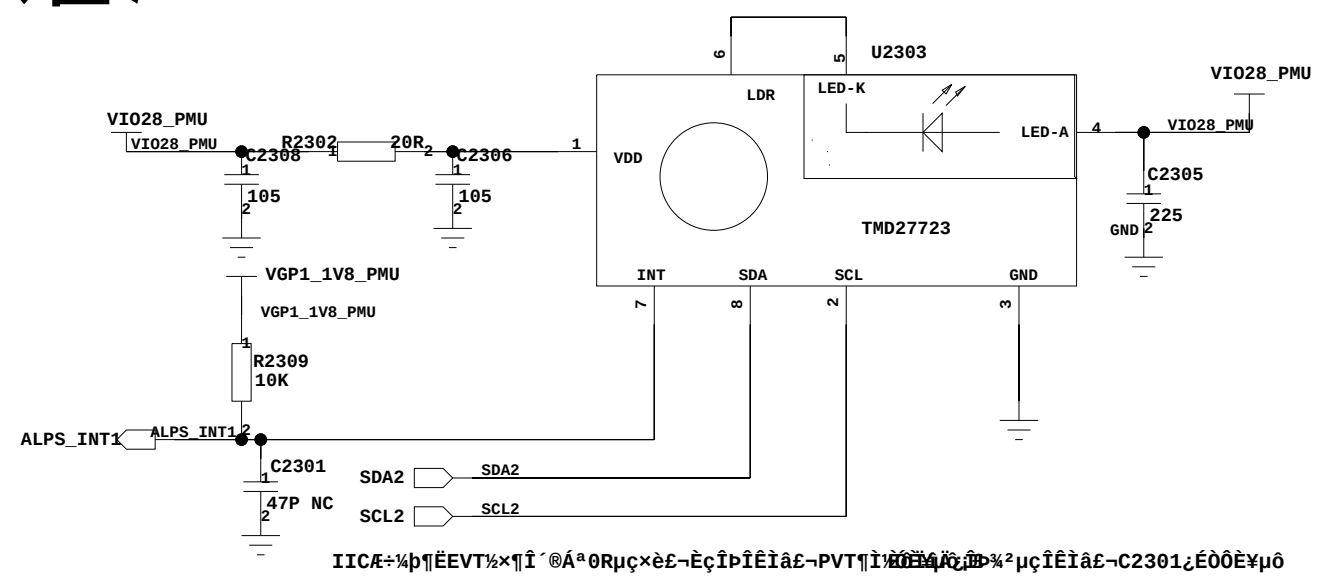
KEY



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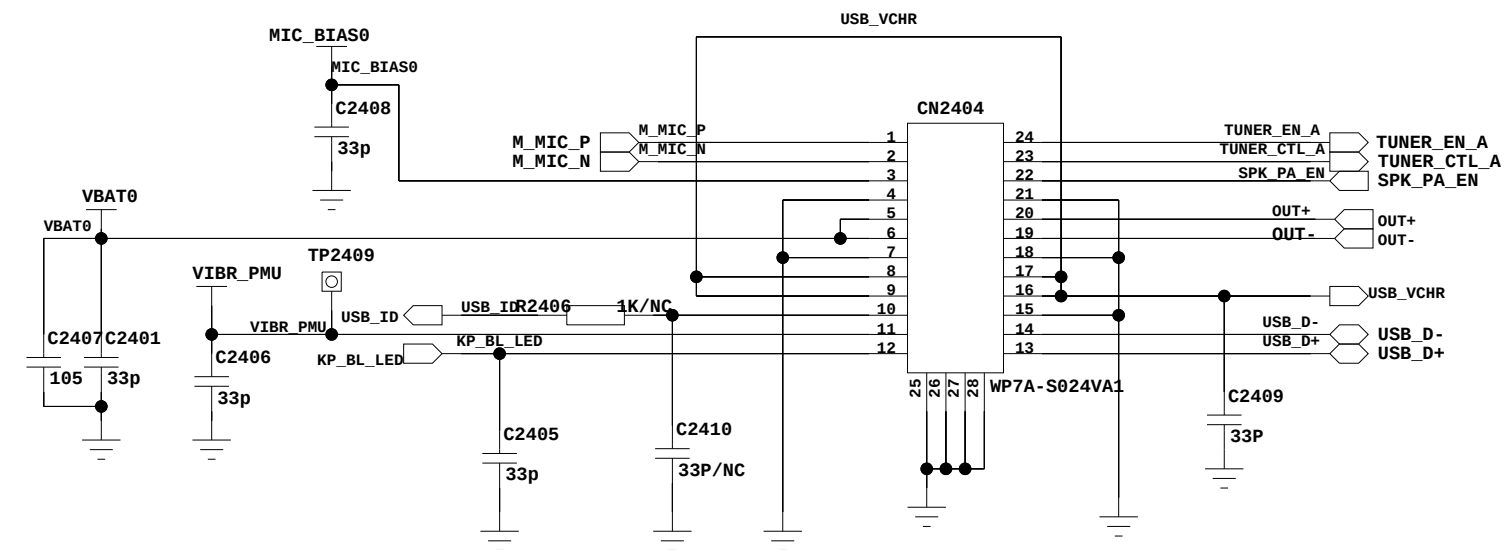


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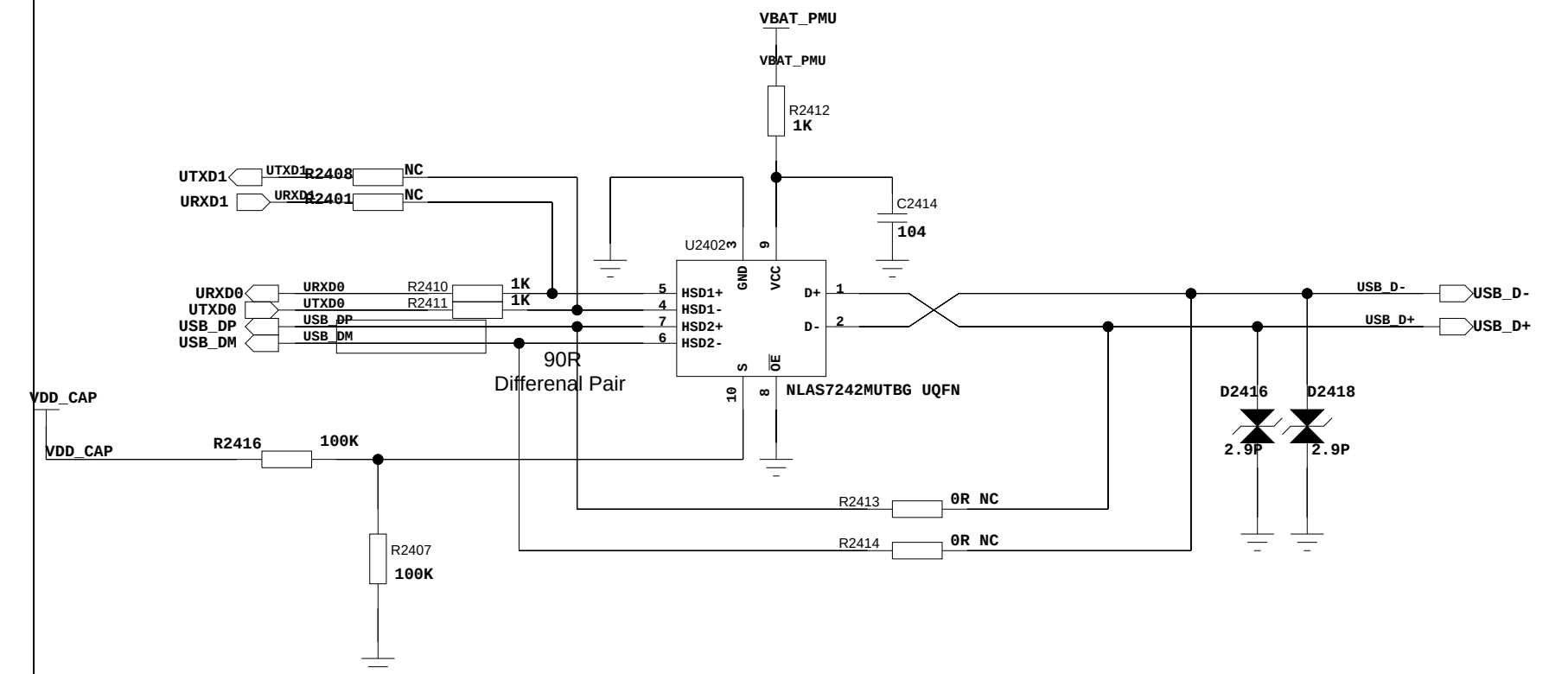


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Drawn By			Draw No: 8	
Checked By			Sheet of 23_SENSOR+KEY+CTP	Ver:
Standard By				
Approved By				

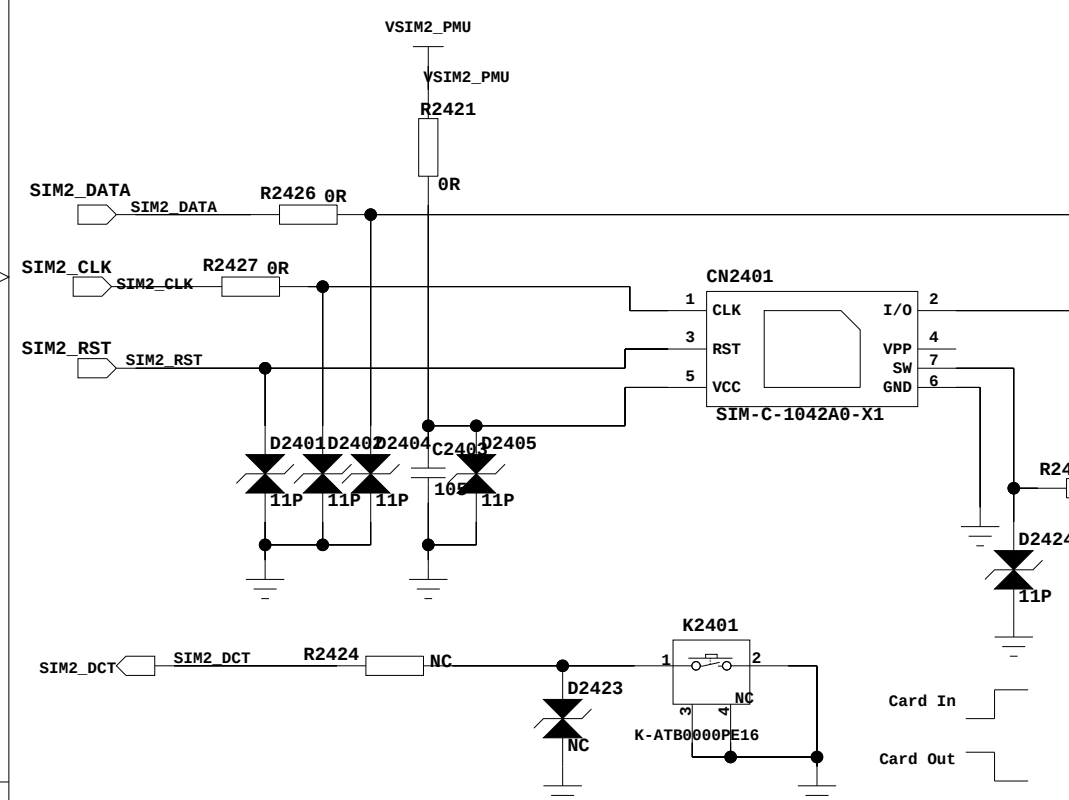
Sub board connector



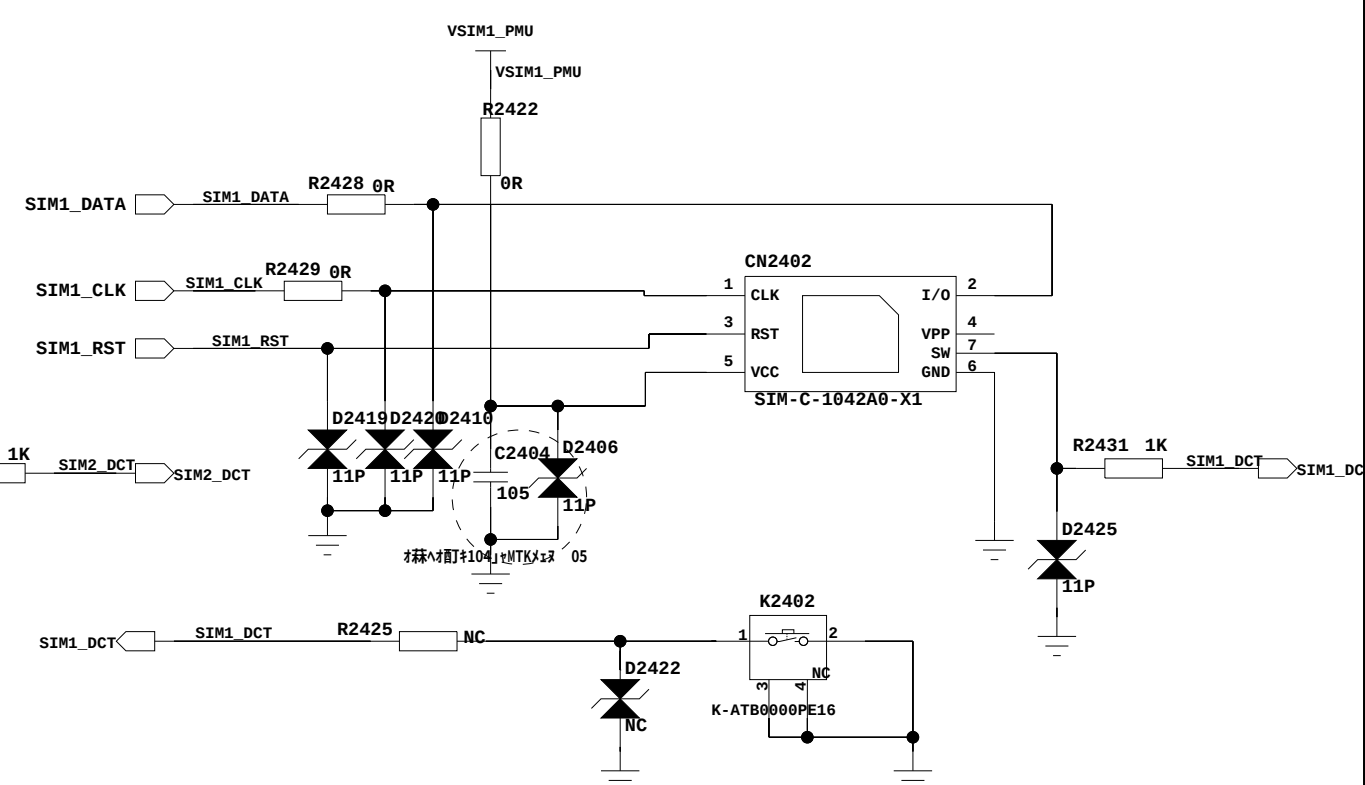
USB 1.0



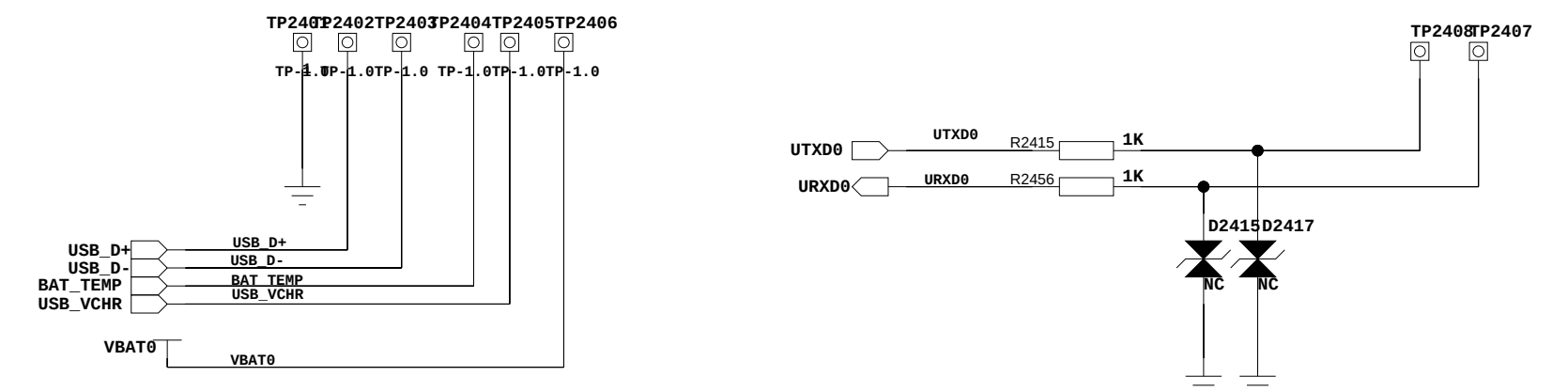
SIM2 Card



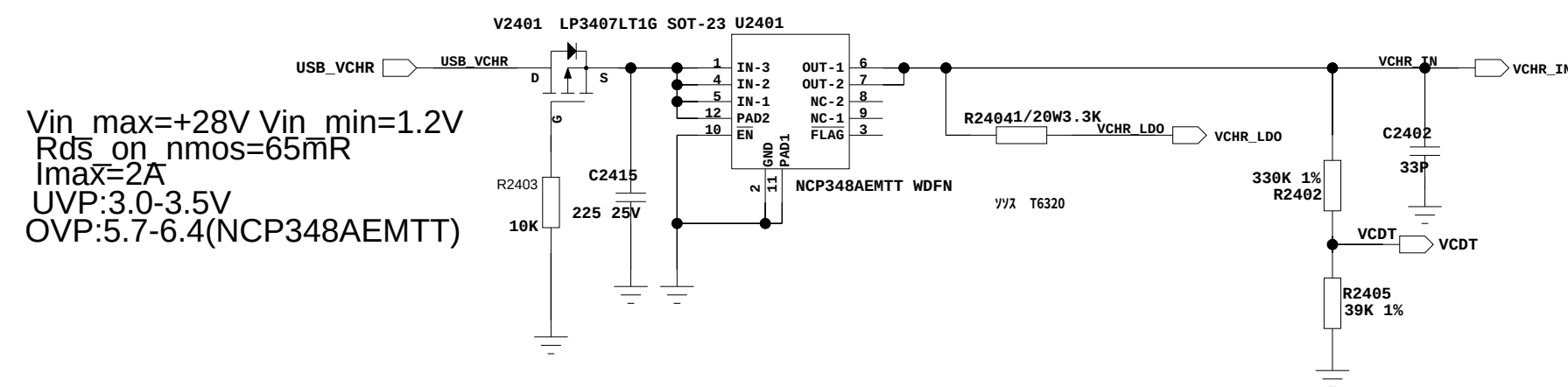
SIM1 Card



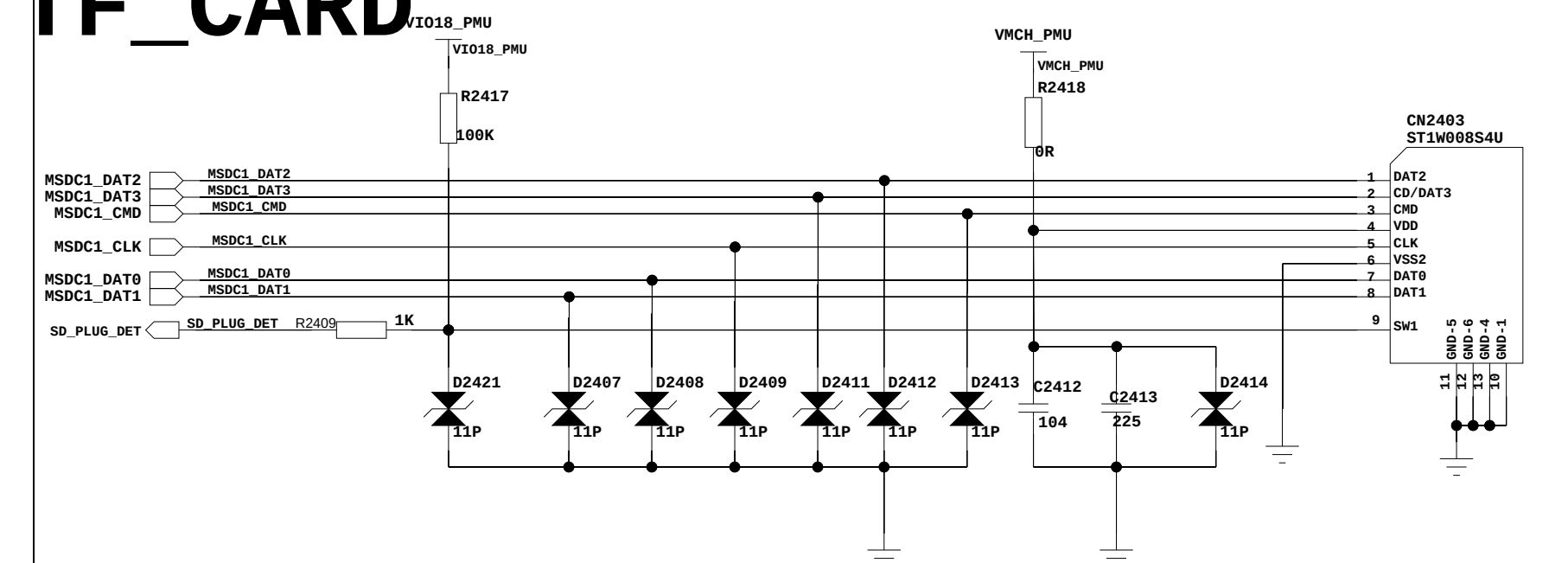
TEST POINT



OVP



TF_CARD



	Sign	Date	Type:
Drawn By			Draw No: 13033
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Approved By			

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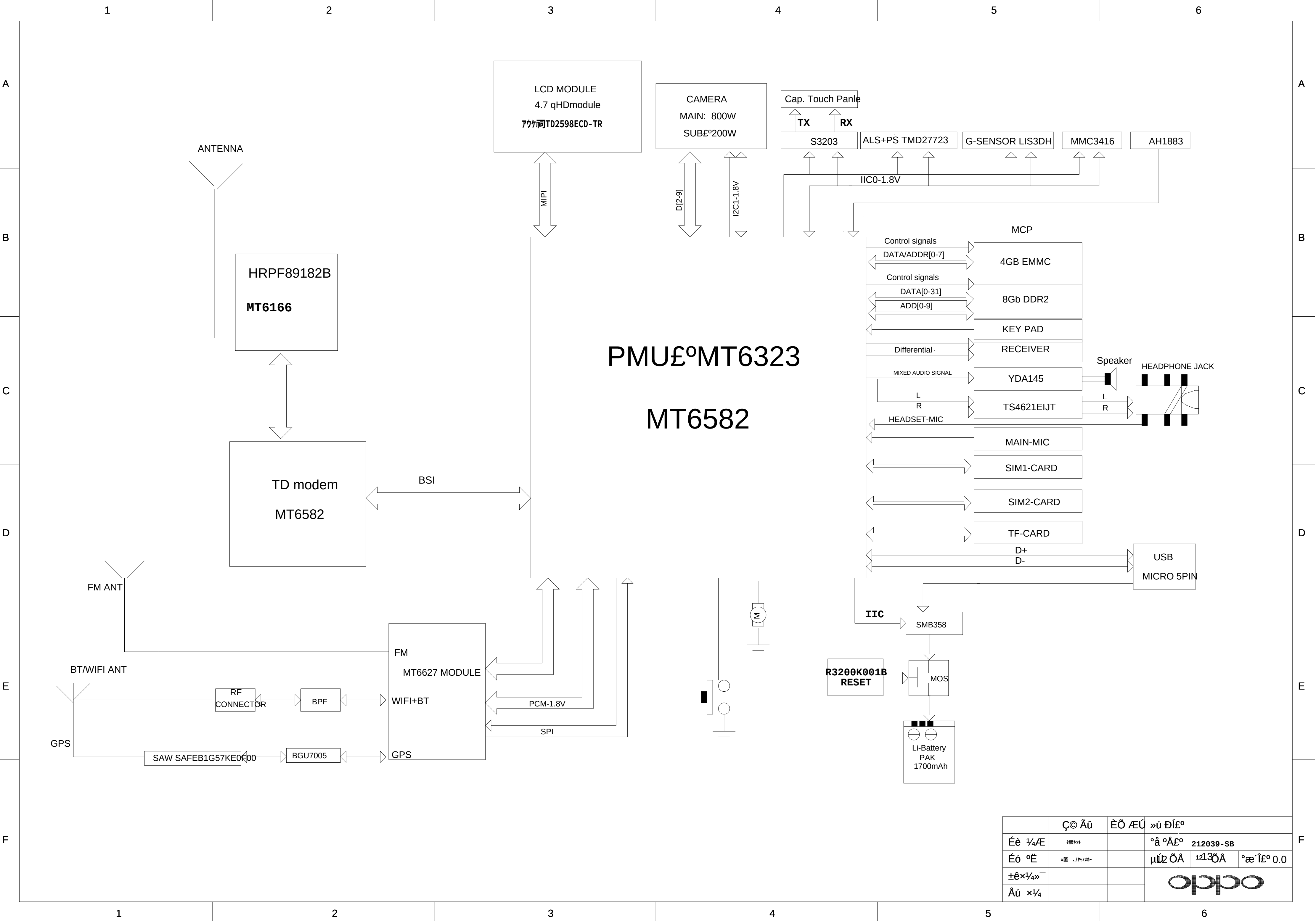
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